

	B.E. (Computer Engineering)						S.E. SEM: III			
Course Name: Mathematics III (Applied)				Course Code:PCC-CS301						
Tea	Teaching Scheme (Program Specific) Examination					on Scheme (Formative/ Summative)				
Mode	s of Teach	ing / Learn	ing / Weig	ghtage	Mo	des of (Continuous Assess	sment / Evalu	ation	
	Но	urs Per We	eek		Theory (100)Practical/Oral (25)TermWork (25)			Theory (100)		Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW		
3	1	-	4	4	25	75	-	25	125	
		IA: In-S	Semester A	ssessmen	t - Pap	per Dur	ration – 1.5Hours			
		ESE: End	l Semester	· Examina	ntion -	Paper 1	Duration - 3 Hou	rs		
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)										
Prerequ	iisite: Basi	c Mathemat	tics							

<u>Course Objective</u>: The objective of the course is to understand the notion of mathematical thinking, proof and logic to solve the problems and apply the knowledge of sets, relations, functions, graphs, lattices and coding theory in programming applications.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Apply the notion of mathematical thinking, mathematical proofs for problem solving.	L1, L2, L3
2	Make use of logic and reasoning for problem solving.	L1, L2, L3
3	Apply arithmetic modulo to design security problems.	L1, L2, L3
4	Make use offunctions and graphs in programming applications.	L1, L2, L3
5	Make use of basic mathematical objects such as functions and relations.	L1, L2, L3
6	Apply discrete structures into other computing problems	L1, L2, L3



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Detailed Syllabus:

Module No.	Topics	Hrs	Cognitive levels of attainment as per Bloom's
1			Taxonomy
I	Introduction to Set Theory and Proofing Techniques	-	
	Definition of Sets, Venn Diagrams, Complements, Cartesian products,	6	
	Power sets, Counting principle, Cardinality and Countability	6	
	(Countable and Uncountable sets)		L1, L2, L3
	Laws of set theory, Fundamental Product, Partitions of sets. The		
	Mathematical Induction		
2	Logia		
2	Propositions and logical operations. Truth tables Equivalence		111713
	Implications Laws of logic Normal Forms Predicates and Quantifiers	6	L1, L2, L3
2	Madulan Arithmatic	Ũ	
3	Modular Arithmetic	-	
	Midulo, Congruence, Primes and the Sieve of Eratosthenes, Testing for	6	L1, L2, L3
	theorems Chinese remainder theorem	0	
	Craph Theory		
т	Definitions of graphs digraphs Multigraphs Paths and		
	cycles(Hamiltonian and Eulerian). Subgraphs, Isomorphism, Special	8	L1, L2, L3
	kinds of graphs: trees, bipartite graphs, planer graphs.	-	
5	Relation and Functions		
	Relation: Definition, types of relation, composition of relations,		
	pictorial representation of relation (Digraphs), properties of relation,	10	
	partial ordering relation. Operations on relations, Closures, Warshall's		L1, L2, L3
	algorithm. Hasse diagram and Lattice.		
	Function: Definition and types of function, composition of		
	functions.Recursive and recursively defined functions.Generating		
	Functions.		
6	Algebraic Structures		
	Algebraic structures with one binary operation: Semigroup, Monoid and	0	
	Group, Abeliangroup, Cyclic groups, Normal subgroups.	9	L1, L2, L3
	Homomorphism, Isomorphism and Automorphism.		· ·
	Coding theory: Coding of binary information and error detection,		
		15	
	l'otalHours	45	

Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Elements of Discrete	C. L. Liu and D. P.	McGraw	SecondEdition	2010
	Mathematics	Mohapatra	Hill	SecondEdition	2010
2	Discrete Mathematical		McGraw		
	Structures with Applications to	J. P. Trembley, R. Manohar	Hill	Fifth Edition	2011
	Computer Science				
3	Disarata Mathematics	Seymour Lipschutz, Marc	McGraw	Fifth Edition	2010
	Discrete Mathematics	Lars Lipson,"	Hill	FILLI Edition	2010



Online References:

Sr.	Website Name	URL	Modules
No.			Covered
1	nptel.ac.in	https://nptel.ac.in/courses/111106086/2	M1- M5
2	www.geeksforgeeks.org	https://www.geeksforgeeks.org/groups-discrete-mathematics/	M6
3	www.tutorialspoint.com	https://www.tutorialspoint.com/graph_theory/	M4

List of Tutorials:

Sr.	Торіс	Hrs.	Cognitive levels of attainment as
No.			per Bloom's Taxonomy
1	Tutorial on Introduction to Set Theory and Proofing	1	L1, L2
	Techniques (I)		
2	Tutorial on Introduction to Set Theory and Proofing	1	L1, L2, L3
	Techniques (II)		
3	Tutorial on Logic	1	L1, L2, L3
4	Tutorial on Modular Arithmetic (I)	1	L1, L2
5	Tutorial on Modular Arithmetic (II)	1	L1, L2, L3
6	Tutorial on Graph Theory (I)	1	L1, L2
7	Tutorial on Graph Theory (II)	1	L1, L2, L3
8	Tutorial on Relation and Functions (I)	1	L1, L2
9	Tutorial on Relation and Functions (II)	1	L1, L2, L3
10	Tutorial on Algebraic Structures (I)	1	L1, L2
11	Tutorial on Algebraic Structures (II)	1	L1, L2, L3
12	Quiz on Set Theory and Logics	1	L1, L2, L3
13	Quiz on Graph Theory	1	L1, L2, L3
14	Group discussion on	1	L1, L2, L3
	Relations and function using think, pair and share		
	technique		
15	Quiz on Algebraic Structures	1	L1, L2, L3
	Total Hours	15	



	B.I	E. (Compu	ter Engine	eering)			S.E.	SEM : III	
Course Name : Data Structures				Course Co	de :PCC- CS3	02			
Теа	aching Sch	eme (Prog	Program Specific) Examination Scheme (Formative/ Sur				ative/ Summa	ntive)	
Modes of Teaching / Learning / Weightage				Modes of Continuous Assessment / Evaluation			ation		
Hours Per Week				The (1)	ory 00)	Practical/Oral (25)	Term Work (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2	5	4	25	75	25	25	150
	IA: In-Semester Assessment - Paper Duration – 1.5 Hours								
		ESE: End	Semester	· Examina	tion -	Paper 1	Duration - 3 Hou	rs	

The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)

Prerequisite: Computer Basics, Procedural Programming Languages

<u>Course Objective</u>: The course intends to deliver the fundamentals of data structures by providing a platform to learn, compare and apply them in real world scenario.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

Sr.	Course Outcomes	Cognitive levels of
190.		Bloom's Taxonomy
1	Compare linear and non-linear data structures.	L1, L2
2	Apply operations like insertion, deletion, searching and traversing on stack and queue data structure.	L1, L2, L3
3	Apply operations like insertion, deletion, searching and traversing on linked list data structure.	L1, L2, L3
4	Apply operations like insertion, deletion, searching and traversing on tree data structure.	L1, L2, L3
5	Apply operations like insertion, deletion, searching and traversing on graph data structure.	L1, L2, L3
6	Analyze appropriate sorting and searching technique for given problem.	L1, L2, L3, L4



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Detailed Syllabus:

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction to Data Structure		
	Introduction, Types of data Structures, Abstract data type, Operations on data structures.	3	L1, L2
2	Stacks and Queues		
	Stack: ADT of stack, operations on stack, array implementation of		
	stack, applications of stack.	7	L1, L2, L3
	queue. Types of queues: circular queue, priority queue, double ended		
	queue, applications of queue.		
3	Linked lists		
	Linked list: ADT of Linked lists, operations on linked list, Types of		
	linked lists: Single linked list, Double Linked list, Implementation of	8	L1, L2, L3
	linked list, stack implementation using linked list, queue		
1	Introduction to Non Linear Data Structure		
-	Trees: Terminologies, Binary tree and its types, Binary tree		
	operations and implementation, Tree traversing techniques,	13	L1, L2, L3
	Expression tree, AVL tree, Multiway search tree, Application of tree.		
5	Graphs		
	Graph: Terminologies, Graph representation: Matrix and Adjacency	6	L1, L2, L3
	list, Graph traversing techniques: BFS, DFS, Applications of graph.		
6	Searching and Sorting		
	Searching: Linear search, binary search		
	Sorting: Insertion sort, Merge sort, Quick sort	8	L1, L2, L3, L4
	Hashing: Hash functions, Hash table, Hashing technique, Collision		
	resolution teeninque		
	TotalHours	45	

Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Data Structures using C	ReemaThareja	Oxford	Second Edition	2014
2	Data Structures: A	Richard F.	CENGAGE	Second Edition	2011
	Pseudocode Approach	Gilberg&Behrouz A.,	Learning		
	with C	Forouzan			
3	Data Structures Using	Aaron M Tenenbaum,	Pearson	Second Edition	2006
	С	YedidyahLangsam,			
		Moshe J Augenstein			
4	Data Structures with C	SeymoreLipschutz	Tata	India Special	2011
		-	McGraw-Hill	Edition	



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Online References:

Sr. No.	Website Name	URL	Modules Covered
1	www.geeksforgeeks.org	https://www.geeksforgeeks.org/stack-data- structure/	M1-M6
2	www.studytonight.com	https://www.studytonight.com/data- structures/introduction-to-data-structures	M1-M3, M6
3	www.w3schools.in	https://www.w3schools.in/category/data- structures-tutorial/	M1-M4, M6

List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Basic Experiments	Build a Program for stack using an array (Menu driven program)	2	L1, L2, L3
2		Build a Program for Queue using an array. (Menu driven program)	2	L1, L2, L3
3		Develop a code for circular queue. (Menu driven)	2	L1, L2, L3
4		Develop a code for Single Linked List. (Menu driven program)	2	L1, L2, L3
5		Develop a code for Doubly linked list. (Menu driven program)	2	L1, L2, L3
6		Develop a code for Binary Search Tree (Menu driven program)	2	L1, L2, L3
7	Design Experiments	Develop a code for BFS. (Menu driven program)	2	L1, L2, L3
8		Develop a code for DFS. (Menu driven program)	2	L1, L2, L3
9		Develop a code for Binary search technique.	2	L1, L2, L3
10		Develop a code for Quick Sort.	2	L1, L2, L3
11	Advanced Experiments	Develop a code for circularly linked doubly linked list.	2	L1, L2, L3
12		Develop a code for Hashing technique with collision resolution.	2	L1, L2, L3
13	Mini/Minor Projects/ Seminar/ Case Studies	Case study:1. Red-Black tree2. Binomial heap Mini Project:1. Build a Snakes & Ladders game2. Sudoku Solver3. Maze generator4. Dictionary implementation5. Employee Record System6. Super market Billing System	6	L1, L2, L3, L4
		TotalHours	30	



B.E. (Computer Engineering)					S.E.	SEM : III			
	Course Name : Database Management System					Course Code : PCC- CS303			
Teaching Scheme (Program Specific)					Examination so	cheme			
Mode	s of Teach	ing / Learn	ing / Weig	ghtage	Moo	les of (Continuous Asses	sment / Evalu	ation
	Но	urs Per We	eek		The	ory	Practical/Oral	Term	Total
					(10	0)	(25)	Work (25)	
Theory	Tutorial	Practical	Contact	Credits	IA	ESE	PR/OR	TW	
			Hours						150
3	1	2	6	5	25	75	25	25	
		IA: In-Se	emester A	ssessment	t - Pap	er Dur	ation – 1.5 Hours		
		ESE: End	Semester	Examina	tion - I	Paper 1	Duration - 3 Hou	rs	
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely									
completion of practical (40%) and Attendance/Learning Attitude (20%)									
Prerequ	isite: Com	puter Basic	S						

<u>Course Objective</u>: The course intends to deliver the fundamental knowledge of database management system and apply this knowledge for implementing and analyzing real world problems.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

SN	Course Objectives	Cognitive levels of attainment as per Bloom's Taxonomy
1	Demonstrate the fundamental elements of relational database Management Systems	L1, L2
2	Outline ER and EER diagram for the real life problem and convert it to Relational Database.	L1, L2,L3
3	Solve and build basic SQL Queries on given Data.	L1, L2, L3
4	Solve and build Advanced SQL Queries on given Data.	L1, L2, L3
5	Develop a relational database using concept of functional dependencies.	L1, L2, L3
6	Interpret the concepts of transaction, concurrency and recovery	L1, L2

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction to Database Concepts	3	L1, L2
	Basic Concepts of Data, Database and DBMS, Applications of Databases,		
	Advantages of Databases over File Processing System, 3 Level Architecture of		
	Database System, Data Abstraction and Data Independence, Database		
	Languages, Database Users, Database Administrator and its functions, Overall		
	System Structure.		
2	Entity Relationship Model(ER), Relational Model and Extended ER Model	6	

S Mart E	TCET DEPARTMENT OF COMPUTER ENGINEERING (COI (Accredited by NBA for 3 years, 3 rd Cycle Accreditation w.e.f. 1 st July 20 Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	MP) 19) 褑	Bettel in 2001
	The Entity-Relationship (ER) Model: Entity with its types, Attributes with its		L1, L2,L3
	types, Relationships with its Types. Real life Examples of ER Diagram.		
	Relational Model: Structure of Relational Databases, Keys with its Types		
	Extended ER Model (EER): Concept of Specialization, Generalization and		
	Aggregation, Mapping of ER and EER to Relational Model.		
3	Introduction to Structured Query Language (SQL)	9	L1, L2, L3
	Overview of SQL, Data Definition Language Commands, Data Manipulation		
	Language Commands, Data Control Language Commands, Transaction Control		
	Language Commands, Constraints, Set and String Operations, Aggregate		
	Functions, Group by and Having Clause.		
4	Advanced SQL with Integrity, Security and Authorization	11	L1, L2, L3
	Nested Sub queries, Referential Integrity in SQL, Joins, Views, Assertion,		
	Trigger, Database Security and Authorization, Granting of Privileges, Revoking		
	of Authorization in SQL		
	Relational Algebra: Fundamental Operations in Relational Algebra		
5	Relational Database Design	8	L1, L2, L3
	Pitfalls in Relational Database Design, Concept of Normalization, Functional		
	Dependencies, 1 NF, 2 NF, 3 NF, BCNF, 4 NF		
6	Transaction, Recovery and Concurrency Control	8	L1, L2
	Transaction Management: Transaction Concept, Transaction States, ACID		

Transaction Management: Transaction Concept, Transaction States, ACID		
Properties of Transaction, Serial and Concurrent Executions, Conflict and View		
Serializability.		
Concurrency Control: Lock Based Protocols, Deadlock Handling		
Recovery: Failure Classification, Log based recovery, Checkpoint, Shadow		
Paging.		
TotalHours	45	

Books and References:

Sr. No.	Title	Authors	Publisher	Edition	Year
1	Database System Concepts	Korth, Slberchatz,Sudarshan	McGraw Hill	Seventh Edition	2019
2	Fundamentals of Database Systems	Elmasri and Navathe	Pearson education	Seventh Edition	2016
3	Database Management Systems	Raghu Ramkrishnan and Johannes Gehrke	McGraw Hill	Third Edition	2014

Online References:

Sr. No.	Website Name	URL	Modules Covered
1	www.guru99.com	https://www.guru99.com/dbms-tutorial.html	M1,M2,M6
2	www.javatpoint.com	https://www.javatpoint.com/dbms-tutorial	M1-M6
3	www.studytonight.co m	https://www.studytonight.com/dbms/	M1 to M3,M5
4	www.w3schools.in	https://www.w3schools.in/dbms/ https://www.w3schools.com/sql/default.asp	M1,M2,M5,M6 M3,M4
5	www.geeksforgeeks.or g	https://www.geeksforgeeks.org/dbms/	M1- M6
6	www.tutorialcup.com	https://www.tutorialcup.com/dbms	M1, M2, M5,M6



List of Practical/ Experiments:

Practical	Type of Experiment	Practical/ Experiment Topic	Hrs.	Cognitive
Number				levels of
				attainment as
				Taxonomy
1		Identify any real life Database		
	Basic Experiments	Management System. Identify Entity,		L1, L2
		Relationship and Attributes with its types	2	
		for Identified Real life Example		
2		Develop an Entity-Relationship (ER)		
		diagram for the problem definition		L1, L2, L3
		Identified and convert it into Relational	2	
		Database.		
3	Design Francisconte	Apply DDL Commands to Specified	2	L1, L2, L3
	Design Experiments	System	2	
4		Apply Basic DML Commands to	2	L1, L2, L3
5		Apple Constraints for the Specified	2	
5		system	2	L1, L2, L3
6		Apply Set and String Operations to	2	L1 L2 L3
0		Specified System	-	21, 22, 20
7		Apply Aggregate	2	L1, L2, L3
		Functions and Create Views for Specified		
		System		
8		Build Nested Queries on Specified	2	L1, L2, L3
		System.		
9		Apply Referential Integrity on Specified	_	L1, L2, L3
		System.	2	
10		Develop of Normalized Database for any	-	
		Real World Example by applying concept	2	L1, L2, L3
11	A J	of Normalization	2	
11	Advanced	Apply Inggers in SQL for Specified	Z	L1, L2, L3
12	Experiments	Apply Joing in SQL for Specified System	2	111212
12		Apply Johns III SQL for Specified System		L1, L2, L3
13	Mini/Minor	1. Student Management System 2 Library Management System		111213
15	Projects/ Seminar/	3 Airline Reservation System	6	L1, L2, L3
	Case Studies	4. Hospital Management System	0	
	Cuse Studies	5. Hotel Management System		
		6. Billing System		
		Total Hours	30	



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List of Tutorials:

Tutorial	Торіс	Hrs.	Cognitive levels of attainment as
Number			per Bloom's Taxonomy
1	Solve and Build SQL Queries on DDL	1	L1, L2, L3
	Commands.		
2	Solve and Build SQL Queries on DML	1	L1, L2, L3
	Commands		
3	Solve and Build SQL Queries on Constraints in	1	L1, L2, L3
	SQL		
4	Solve and Build SQL Queries on Aggregate	1	L1, L2, L3
	Functions.		
5	Solve and Build SQL Queries on Set Functions.	1	L1, L2, L3
6	Solve and Build SQL Queries on String	1	L1, L2, L3
	Operations		
7	Solve and Build SQL Queries on Group by and	1	L1, L2, L3
	Having Clause		
8	Outline ER Diagram for given real life problem	1	L1, L2, L3
	and convert it into relational Database.		
9	Solve and Build SQL Nested Queries	1	L1, L2, L3
10	Solve and Build SQL Queries on Referential	1	L1, L2, L3
	Integrity		
11	Solve and Build SQL Queries on Joins	1	L1, L2, L3
12	Solve Build SQL Queries on Real Time	1	L1, L2, L3
	Management Systems.		
	Develop Database design by applying concept of	1	L1, L2, L3
13	Normalization to Student Management System		
14	Develop Database design by applying concept of	1	L1, L2, L3
	Normalization to Hospital Management System		
15	Develop Database design by applying concept of	1	L1, L2, L3
	Normalization to Airlines Reservation System		
	Total Hours	15	



B.E. (Computer Engineering)					S.E.	SEM : III					
Course Name : Digital Logic Design & Analysis					Course Code :ESC301						
Teaching Scheme (Program Specific) Examination					on Scheme (Form	ative/ Summa	tive)				
Mode	s of Teach	ing / Learn	ing / Wei	ghtage	Mo	des of (Continuous Assess	sment / Evalu	ation		
	Но	urs Per Wo	eek		Theory (100)		Theory (100)		Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW			
3	-	2	5	4	25	75	25	25	150		
		IA: In-S	emester A	ssessmen	t - Pap	er Dur	ation – 1.5 Hours				
	ESE: End Semester Examination - Paper Duration - 3 Hours										
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)											
Prerequ	iisite: Engi	neering Ma	thematics								

<u>Course Objective</u>: The course intends to provide the basic knowledge of digital logic levels and apply knowledge to understand digital electronics circuits.

Course	Outcomes: Upon	completion o	f the course	students w	ill be able to:
	I	.			

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Build different number systems forms	L1, L2, L3
2	Solve Boolean expressions	L1, L2, L3
3	Explain the basics of TTL and CMOS logic families	L1, L2
4	Illustrate the design of Combinational circuits	L1, L2,L3
5	Illustrate the design of Sequential circuits	L1, L2,L3
6	Understand the concepts in designing of counters and registers	L1, L2, L3



1 Number Systems and codes attainment as per Bloom's Taxonomy 1 Number Systems and codes attainment as per Bloom's Taxonomy 1 Introduction to number system and conversions: Binary, Octal, Decimal and Hexadecimal number Systems, Binary arithmetic: addition, subtraction (1''s and 2''s complement), multiplication and division. Octal and Hexadecimal arithmetic: Addition and Subtraction (7''s and 8''s complement method for octal) and (15''s and 16''s complement method for oteral). Codes: Gray Code, BCD Code, Excess-3 code, ASCII Code. Error Detection and Correction: Hamming codes. 7 L1, L2, L3 2 Boolean Algebra & Logic Gates 7 L1, L2, L3 7 L1, L2, L3 3 Don't care condition, Quine-McClusky Method, NAND,NOR, EXOR, EXNOR, Positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, bon't care condition, Quine-McClusky Method, NAND,NOR Realization. 3 11, L2, L3 4 Analysis and Design of Combinational Logic 3 11, L2, L3 4	Module No.	Topics	Hrs.	Cognitive levels of
1 Number Systems and codes 8 L1, L2, L3 Introduction to number systems, sinary arithmetic: addition, subtraction (1''s and 2''s complement), multiplication and division. Octal and Hexadecimal arithmetic: Addition and Subtraction (7''s and 8''s complement method for octal) and (15''s and 16''s complement method for Hexadecimal). Codes: Gray Code, BCD Code, Excess-3 code, ASCII Code. Error Detection and Correction: Hamming codes. 8 L1, L2, L3 2 Boolean Algebra & Logic Gates 7 L1, L2, L3 Theorems and Properties of Boolean Algebra, Boolean functions, Boolean function reduction using Boolean laws, Canonical forms, Standard SOP and POS form. 7 L1, L2, L3 3 Thoroduction, Quine-McClusky Method, NAND, NOR, EXOR, EXNOR, positive and negative logic, K-map method 2 variable, 4 variable, 4 variable, Don't care condition, Quine-McClusky Method, NAND,NOR Realization. 7 L1, L2 3 Introduction: Terminologies like Propagation Delay, Power Consumption, Fan in and Fan out, current and voltage parameters, noise margin, with respect to TTL and CMOS Logic and their comparison 3 4 4 Analysis and Design of Combinational Logic 9 L1, L2, L3 5 Latches and Flip Flops 9 L1, L2, L3 6 Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP-DOWN counter. 9 L1, L2, L3 6 Counters: Design of Asynchronous	110.			attainment as per Bloom's Taxonomy
Introduction to number system and conversions: Binary, Octal, Decimal and Hexadecimal number Systems, Binary arithmetic: addition, subtraction (1°s and 2°s complement), multiplication and division. Octal and Hexadecimal arithmetic: Addition and Subtraction (7″s and 8″s complement method for octal) and (15″s and 16″s complement method for Hexadecimal). Codes: Gray Code, ECD Code, Excess-3 code, ASCII Code. Error Detection and Correction: Hamming codes.8L1, L2, L32Boolean Algebra & Logic Gates Theorems and Properties of Boolean Algebra, Boolean functions, Boolean POS form. Basic Digital gates: NOT, AND, OR, NAND, NOR, EXNOR, positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, Don't care condition, Quine-McClusky Method, NAND,NOR Realization.7L1, L2, L33Digital Logic Families Introduction: Terminologies like Propagation Delay, Power Consumption, 	1	Number Systems and codes		
2 Boolean Algebra & Logic Gates 7 L1, L2, L3 7 L1, L2, L3 7 L1, L2, L3 8 Don't care condition, Quine-McClusky Method, NAND,NOR , EXOR , EXNOR, positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, Don't care condition, Quine-McClusky Method, NAND,NOR Realization. 7 L1, L2, L3 3 Digital Logic Families 1 L1, L2 L1, L2 4 Analysis and Design of Combinational Logic 3 1 L1, L2, L3 5 Introduction: SR latch, Concepts of Flip Flops 9 L1, L2, L3 6 Counters and Flip Flops 9 L1, L2, L3 6 Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP- DOWN counter. 9 L1, L2, L3 9 L1, L2, L3 L1, L2, L3 L1, L2, L3		Introduction to number system and conversions: Binary, Octal, Decimal and Hexadecimal number Systems, Binary arithmetic: addition, subtraction (1"s and 2"s complement), multiplication and division. Octal and Hexadecimal arithmetic: Addition and Subtraction (7"s and 8"s complement method for octal) and (15"s and 16"s complement method for Hexadecimal). Codes: Gray Code, BCD Code, Excess-3 code, ASCII Code. Error Detection and Correction: Hamming codes.	8	L1, L2, L3
Theorems and Properties of Boolean Algebra, Boolean functions, Boolean function reduction using Boolean laws, Canonical forms, Standard SOP and POS form. Basic Digital gates: NOT , AND , OR , NAND , NOR , EXOR , EXNOR, positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, Don't care condition, Quine-McClusky Method, NAND,NOR Realization.7L1, L2, L33Digital Logic Families Introduction: Terminologies like Propagation Delay, Power Consumption, Fan in and Fan out, current and voltage parameters, noise margin, with respect to TTL and CMOS Logic and their comparison3L1, L2, L34Analysis and Design of Combinational Logic Introduction, Half and Full Adder, Half subtractor Full Subtractor, one digit BCD Adder, Multiplexer, Multiplexer tree, Demultiplexer, Demultiplexer tree, Encoders Priority encoder, Decoders, One bit, Two bit, 4-bit Magnitude Comparator, ALU IC 74181.9L1, L2, L35Latches and Flip Flops and Excitation Tables of all types, Race around condition, Master Slave J- K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state diagrams, State table, concept of Moore and Mealy machine9L1, L2, L36Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP-DOWN counter. Shift Register, Ring and twisted ring/Johnson Counter, sequence generator.9L1, L2, L3	2	Boolean Algebra & Logic Gates		
Basic Digital gates: NOT, AND, OR, NAND, NOR, EXOR, EXOR, EXNOR, positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, 5 variable, 0 on't care condition, Quine-McClusky Method, NAND,NOR Realization. Introduction, Quine-McClusky Method, NAND,NOR Realization. Introduction: Terminologies like Propagation Delay, Power Consumption, Fan in and Fan out, current and voltage parameters, noise margin, with respect to TTL and CMOS Logic and their comparison 3 L1, L2 4 Analysis and Design of Combinational Logic 3 Introduction, Half and Full Adder, Half subtractor Full Subtractor, one digit BCD Adder, Multiplexer, Multiplexer tree, Demultiplexer, Demultiplexer tree, Encoders Priority encoder, Decoders, One bit, Two bit, 4-bit Magnitude Comparator, ALU IC 74181. 9 L1, L2, L3 5 Latches and Flip Flops 9 L1, L2, L3 6 Counters and Shift registers 9 L1, L2, L3 6 Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters; UP- DOWN counter. 9 L1, L2, L3 6 Counters: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator. 9 L1, L2, L3		Theorems and Properties of Boolean Algebra, Boolean functions, Boolean function reduction using Boolean laws, Canonical forms, Standard SOP and POS form.	7	L1, L2, L3
positive and negative logic, K-map method 2 variable, 3 variable, 4 variable, bon't care condition, Quine-McClusky Method, NAND,NOR Realization.Image: Condition of the condition.Is and the condition of the condition.Is and the condition of the condition of the condition of the condition of the condition.Is and the condition of the condition of the condition of the condition.Is and the condition of the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition.Is and the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition of the condition.Is and the condition of the condition.Is and the condition of the cond		Basic Digital gates: NOT, AND, OR, NAND, NOR, EXOR, EXNOR,		
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4 Analysis and Design of Combinational Logic 9 L1, L2, L3 Introduction, Half and Full Adder, Half subtractor Full Subtractor, one digit BCD Adder, Multiplexer, Multiplexer tree, Demultiplexer, Demultiplexer tree, Encoders Priority encoder, Decoders, One bit, Two bit, 4-bit Magnitude Comparator, ALU IC 74181. 9 L1, L2, L3 5 Latches and Flip Flops 1, L1, L2, L3 Introduction: SR latch, Concepts of Flip Flops: SR, D, J-K, T, Truth Tables and Excitation Tables of all types, Race around condition, Master Slave J-K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state diagrams, State table, concept of Moore and Mealy machine 9 L1, L2, L3 6 Counters and Shift registers 9 L1, L2, L3 Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator. 9 L1, L2, L3		Introduction: Terminologies like Propagation Delay, Power Consumption, Fan in and Fan out, current and voltage parameters, noise margin, with respect to TTL and CMOS Logic and their comparison	3	11, 12
Introduction, Half and Full Adder, Half subtractor Full Subtractor, one digit BCD Adder, Multiplexer, Multiplexer tree, Demultiplexer, Demultiplexer tree, Encoders Priority encoder, Decoders, One bit, Two bit, 4-bit Magnitude Comparator, ALU IC 74181.95Latches and Flip Flops5Latches and Flip Flops9Introduction: SR latch, Concepts of Flip Flops: SR, D, J-K, T, Truth Tables and Excitation Tables of all types, Race around condition, Master Slave J- K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state diagrams, State table, concept of Moore and Mealy machine96Counters and Shift registers 	4	Analysis and Design of Combinational Logic		L1, L2, L3
5 Latches and Flip Flops L1, L2, L3 Introduction: SR latch, Concepts of Flip Flops: SR, D, J-K, T, Truth Tables and Excitation Tables of all types, Race around condition, Master Slave J- K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state diagrams, State table, concept of Moore and Mealy machine 9 L1, L2, L3 6 Counters and Shift registers 9 L1, L2, L3 6 Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP- DOWN counter. Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator. 9		Introduction, Half and Full Adder, Half subtractor Full Subtractor, one digit BCD Adder, Multiplexer, Multiplexer tree, Demultiplexer, Demultiplexer tree, Encoders Priority encoder, Decoders, One bit, Two bit, 4-bit Magnitude Comparator, ALU IC 74181.	9	
Introduction: SR latch, Concepts of Flip Flops: SR, D, J-K, T, Truth Tables 9 and Excitation Tables of all types, Race around condition, Master Slave J- 9 K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state 9 diagrams, State table, concept of Moore and Mealy machine 9 Counters: Design of Asynchronous and Synchronous Counters, Modulus of 9 the Counters, UP- DOWN counter. 9 Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, 9 Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence 9	5	Latches and Flip Flops		L1, L2, L3
6 Counters and Shift registers Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP- DOWN counter. 9 Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator. 9		Introduction: SR latch, Concepts of Flip Flops: SR, D, J-K, T, Truth Tables and Excitation Tables of all types, Race around condition, Master Slave J- K Flip Flops, Timing Diagram, Flip-flop conversion, State machines, state diagrams, State table, concept of Moore and Mealy machine	9	
Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP- DOWN counter.L1, L2, L3Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator.9	6	Counters and Shift registers		
<u>0</u>		Counters: Design of Asynchronous and Synchronous Counters, Modulus of the Counters, UP- DOWN counter. Shift Registers: SISO, SIPO, PIPO, PISO Bidirectional Shift Register, Universal Shift Register, Ring and twisted ring/Johnson Counter, sequence generator.	9	L1, L2, L3
TotalHours 45		TotalHours	45	



Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Modern Digital	R. P. Jain	McGraw Hill	Fourth	2010
	Electronics			Edition	
2	Digital Logic and	M. Morris Mano	Pearson	Sixth Edition	2017
	computer Design				
3	Digital Principles and	Donald p Leach, Albert	McGraw Hill	Seventh	2011
	Applications	Paul Malvino		Edition	

Online References:

Sr. No.	Website Name	URL	Modules
			Covered
1	www.crectirupati.com	http://www.crectirupati.com/sites/default/files/lecture	M11-M2,
		_notes/DLD%20lecture%20notes.pdf	M4-M6
2	www.engrcs.com	https://www.engrcs.com/courses/engr250/engr250lec	M1-M6
		ture.pdf	
3	www.uptusuccess.com	https://uptusuccess.com/digital-logic-design-rec301/	M1-M3

List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1		Utilize logic gates to realize Boolean	2	L1, L2, L3
2	Basic Experiments	Make use of universal gates to implement Basic gates	2	L1, L2, L3
3		Build parity generator and detector.	2	L1, L2, L3
4		Build basic gates using Xilinx.	2	L1, L2, L3
5		Build binary to gray code and gray code	2	L1, L2, L3
		to binary converter		
		Construct arithmetic circuits i) Half	2	L1, L2, L3
6		adder ii) Full adder iii) Half subtractor iv) Full subtractor.		
7	Design Experiments	Construct 4:1 multiplexer using Xlinx.	2	L1, L2, L3
8		Develop full adder using multiplexer IC	2	L1, L2, L3
9		Develop 4 bit binary adder using IC 7483	2	L1, L2, L3
10		Develop full adder using multiplexer IC	2	L1, L2, L3
11		Construction of 2-bit magnitude comparator.	2	L1, L2, L3
12		Make use of NAND and NOR gates to Verify state tables of R-S flip-flop, J - K flip-flop, T Flip-Flop, D Flip-Flop	2	L1, L2, L3

	DEPARTMEN (Accredited by NB Choice Based Credi	FOFC A for 3 ye t Grading Sy Under	TCET OMPUTER ENGINEERIN ears, 3 rd Cycle Accreditation w.e.f. stem with Holistic Student Development (CBCC TCET Autonomy Scheme - 2019	IG (COMP 1 [≋] July 2019) зs - н 2019)	etd. in 2001
13		1.	Water Level Indicator		
	Mini/Minor	2.	Rain Alarm Circuit		
	Projects/ Seminar/	3.	RFID based Attendance	6	L1, L2, L3
	Case Studies		System		
		4.	PC Based Digital IC Tester		
		5.	K-map using 5 Variables		
		6.	Very High Speed Integrated		
			Circuit Hardware Description		

TotalHours

30

Language



B.E. (Computer Engineering)					S.E.	SEM : III			
Cou	Course Name : Computer Organization & Architecture					Course (Code :ESC302		
Teaching Scheme (Program Specific)				Exa	minati	on Scheme (Form	ative/ Summa	ntive)	
Mode	s of Teach	ing / Learn	ing / Wei	ghtage	Мо	des of (Continuous Asses	sment / Evalu	ation
Hours Per Week					Theory (100)		Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2	5	4	25	75	25	25	150
	IA: In-Semester Assessment - Paper Duration – 1.5 Hours								
ESE: End Semester Examination - Paper Duration - 3 Hours									
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)									
Prerequ	isite: Basi	c Mathemat	tics						

<u>Course Objective</u>: This course intends to deliver basics of modern computer organization and architectures, covering the interaction between computer hardware and software at various levels and to analyze performance issues in processor and memory design of a digital computer.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Explain basic structure and working of computer.	L1, L2
2	Applyvarious computer arithmetic operations.	L1, L2, L3
3	Explain the working of control unit.	L1, L2
4	Understandvarious types of memory of digital computer.	L1, L2, L3
5	Compare between different types I/O modes of transfer.	L1, L2
6	Understand basic concepts of pipelining.	L1, L2, L3



Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's
1	Introduction of Commuter Organization and Analytesture		Taxonomy
1	Basic organization of computer organization and Architecture Basic organization of computer and block level description of the functional units. Introduction to computer organization & Architecture, Evolution of Computers, Von Neumann model, Instruction cycle, Addressing Modes, Instruction Format, Introduction toSystem buses, Multi-bus organization.	5	L1, L2
2	Data Representation and Arithmetic Algorithm		L1, L2, L3
	Signed number representation, fixed point computation algorithms, Booth multiplication, Division - non-restoring and restoring techniques, floating point arithmetic algorithms. IEEE 754 floating point number representation.	7	
3	Control Unit Design		L1, L2
	Control Unit: Soft wired (Micro-programmed) and Hardwired control unit design methods, Address sequencing, Microprogram Sequencer, Micro operation, Micro instruction Format, Control Memory, Concepts of nano programming, Introduction to RISC and CISC architectures and design issues.	6	
4	Memory Organization		L1, L2
	Classifications of primary and secondary memories, Types of RAM and ROM, Memory hierarchy and characteristics. Memory Access Methods. Cache memory: concept, architecture, mapping, Cache coherency, Interleaved and Associative memory, Memory management unit, Magnetic Hard disks.	9	
5	I/O Organization		L1, L2
	Input/ Output systems, Types of data transfer techniques: Programmed I/O, Interrupt driven ,Direct Memory Access and DMA controller, Types of Interrupts, Bus Arbitration, Interface circuits - Parallel and serial port. Features of PCI and PCI Express bus.	9	
6	Pipelining & Parallel Processing		L1, L2
	Introduction to pipelining, Performance measures of pipelining, Synchronous and Asynchronous pipelining, Instruction level pipelining (ILP), Pipelining hazards, Handling of Branch instructions. Multiprocessor (loosely & tightly coupled) and Multicomputer(UNA,NUMA,COMA).	9	
	Total Hours	45	
		1	



DEPARTMENT OF COMPUTER ENGINEERING (COMP) [Accredited by NBA for 3 years, 3rd Cycle Accreditation w.e.f. 1st July 2019] Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019



Books and References:

	Title	Authors	Publisher	Edition	Year
1	Computer organization	V. Carl, G. Zvonko and S.	McGraw Hill	Sixth	2011
		G. Zaky		Edition	
2	Computer Architecture and	Morris Mano	McGraw Hill	Third	2010
	organization			Edition	
3	Computer Organizations and	John P. Hayes	McGraw-Hill	Fifth	2017
	Architecture			Edition	

Online Resources:

S. No.	Website Name	URL	Modules
			Covered
1	www.nptel.ac.in	https://nptel.ac.in/courses/106102062/	M1-M6
2	www.edx.org	https://www.edx.org/course/computation-structures-	M1-M6
		2-computer-architecture	
3	www.coursera.org	https://www.coursera.org/learn/comparch	M1-M6

List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	RBT Levels
1	Basic Experiments	Demonstrate Computer Anatomy- Memory, Ports, Motherboard and add-on cards.	2	L1, L2
2		Develop a program to calculate 1's compliment and 2's compliment of a Binary number.	2	L1, L2, L3
3		Develop a program to calculate Binary and octal addition and multiplication	2	L1, L2, L3
4		Construct of Register and Counter	2	L1, L2, L3
5		Develop a program to convert in IEEE 754 format	2	L1, L2, L3
6		Develop a program to perform Booth's Multiplication on binary numbers	2	L1, L2, L3
7		Develop a C/Java program for Non- restoring Division	2	L1, L2, L3
8	Design Experiments	Develop a C/Java program for Restoring Division	2	L1, L2, L3
9	Design Experiments	Experiment with mapping techniques of Direct Mapped Cache memory.	2	L1, L2, L3

Image: Composition of the state of the					
10		Experiment with mapping techniques of	2	L1, L2, L3	
10		Cache memory, Associative Mapped			
		Build a program to compare the			
11		performance measures of pipelined and	2	L1, L2, L3	
		non-pipelined systems.			
12		Build a C/Java program for Interrupt			
		Handling	2	L1, L2, L3	
		1. Case Study: A Recent Intel Processor		L1, L2, L3	
13	Mini/Minor	2. Parallel Architectures			
	Projects/ Seminar/	3. Bus Arbitration	6		
	Case Studies	4. Direct Memory Access			
		5. Cache Mapping			
		6. Nano Programming			
		TotalHours	30		



B.E. (Computer Engineering)					S.E.	SEM: III			
Course Name: Seminar / Workshop					Course	Code: SI301			
Teaching Scheme (Program Specific)				Exa	minati	on Scheme (Form	ative/ Summa	tive)	
Modes of Teaching / Learning / Weightage Modes of Continuous Assessment / Evaluation					ation				
Hours Per Week Theory Practical/Oral Term Total (100) (25) Work (25)						Total			
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
-	-	2	2	1	-	-	-	25	25
IA: In-Semester Assessment									
ESE: End Semester Examination									
Prerequ	isite: Math	nematical Fo	oundation,	Computir	ng Meth	ods			

<u>Course Objective</u>: To familiarize students with emerging technologies used in industry. Also, to expose the students with developments in the various Program Specific Research (PSR) domains offered by the department.

<u>Course Outcomes:</u> Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	To comprehend the different emerging technologies used in the industry	L1, L2
2	To apply different emerging technologies for solving the problems in the domains.	L1, L2,L3

Module	Topics	Hrs	Cognitive levels of
No.			attainment as per
			Bloom's Taxonomy
	Computing and System Design domain		
1	Seminar on Emerging Technologies used in the industry		
	Hands-on Workshop on Industry special skills	5	L1, L2, L5
	Industry Connect / Alumni Connect Seminar		
	Communication Networking and Web Engineering domain		
2	• Seminar on Emerging Technologies used in the industry		
	Hands-on Workshop on Industry special skills	5	L1, L2, L3
	Industry Connect / Alumni Connect Seminar		
	Multimedia System Design and Development domain		L1, L2, L3

TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP) [Accredited by NBA for 3 years, 3 rd Cycle Accreditation w.e.f. 1 st July 2019]	
ENGINEERS Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	Estd. In 2001

3	 Seminar on Emerging Technologies used in the industry Hands-on Workshop on Industry special skills Industry Connect / Alumni Connect Seminar 	5	
	Software Development and Information Management System		
	domain		
4	Seminar on Emerging Technologies used in the industry	5	L1, L2, L3
	Hands-on Workshop on Industry special skills		
	Industry Connect / Alumni Connect Seminar		
	Intelligent System Design and Development		
5	Seminar on Emerging Technologies used in the industry		
	Hands-on Workshop on Industry special skills	5	L1, L2, L3
	Industry Connect / Alumni Connect Seminar		
6	Case study on the Emerging Tools and techniques under Program	5	L1, L2, L3
	Specific Research domains		
	Total Hours	30	
		1	

Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Emerging Technologies in Computer Science: Introducing The New IT & The Internet of Things	Andrew Moss	Amazon	Ι	2019
2	Emerging Technologies in Computing	Miraz, M.H., Excell, P., Ware, A., Soomro, S., Ali, M.	Springer	Ι	2018

Online References:

Sr.	Website Name	URL	Modules
No.			Covered
1	Advanced Topics in	http://m.el-dosuky.com/course.php?c=advanced-topics-in-	M1-M6
	Computer Science	computer-science	
2	https://interestingengine ering.com	https://interestingengineering.com/5-technology-trends-to- watch-in-2019	M1-M6



				S.E. Ser	mester	-III			
	B.I	E. (Compu	ter Engino	eering)			S.E.	SEM : III	
Course Name : Environmental Studies				S		Course	Code :MC301		
Teaching Scheme (Program Specific) Examination Scheme (Formative/ Summative)									
Modes of Teaching / Learning / Weightage Modes of Continuous Assessment / Evaluation									
Hours Per Week				The (1	ory 00)	Practical/Oral (25)	Term Work (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
1	-	-	1	-	-	-	-	25	25
	IA: In-Semester Assessment - Paper Duration – 1.5 Hours ESE: End Semester Examination - Paper Duration - 3 Hours								

The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)

Prerequisite: Biology, chemistry, geography, geology, physics.

<u>Course Objective:</u> The course intends to deliver the fundamental concepts of Environmental Sciences. It will also help in understanding & analyzing the major challenges and current issues in Environment and evaluate possible solutions.

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Relate the concept of Environmental Sciences and provide solutions to the major challenges and current issues in Environment.	L1, L2
2	Relate the fundamentals and importance of Natural Resources and understand the importance of Biodiversity and its Conservation.	L1, L2
3	Interpretand Analyze various types of Environmental Pollution and their effects on plants and animals	L1, L2, L3, L4
4	Relate and Apply various laws available in the country to protect the Environment.	L1, L2, L3
5	Interpret and Analyze address social issues for sustainable development.	L1, L2, L3, L4
6	Relate and Analyze the importance of Environmental Monitoring.	L1. L2. L3. L4

<u>Course Outcome:</u>Upon completion of the course students will be able to:



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1 Concepts of Environmental Sciences 2 L1, L2 Environment, Levels of organizations in environment, Structure and functions in an ecosystem; Biosphere, its Origin and distribution on land in water and in air. 2 L1, L2 2 Natural Resources, Biodiversity and its conservation 3 3 2 Natural Resources, Biodiversity and its conservation 3 1 2 Natural Resources, Biodiversity and its conservation 3 1 2 Natural Resources, Biodiversity and its conservation 3 1 3 Renewable and Non-renewable Resources, Forests, water, minerals, Food and land (with example of one case study); Energy, Growing energy needs, energy sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity. 3 11, L2 3 Environmental Pollution 3 11, L2 4 Environmental Pollution prevention; Management of pollution-Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. 3 11, L2, L3 4 Environmental Biotechnology 2 L1, L2, I Biotechnology for environmental protection- Biological indicators, bio-sensors; Remedial measure	2
Environment, Levels of organizations in environment, Structure and functions in an ecosystem; Biosphere, its Origin and distribution on land in water and in air.2L1, L22Natural Resources, Biodiversity and its conservation Renewable and Non-renewable Resources, Forests, water, minerals, Food and land (with example of one case study); Energy, Growing energy needs, energy sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity.3L1, L23Environmental Pollution Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution- Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management.34Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application2L1, L2, L3	2
2 Natural Resources, Biodiversity and its conservation 3 Renewable and Non-renewable Resources, Forests, water, minerals, Food and land (with example of one case study); Energy, Growing energy needs, energy sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity. 3 L1, L2 3 Environmental Pollution 3 L1, L2 3 Environmental Pollution 3 L1, L2, L3 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, biosensors; Remedial measures- Bio-remediation, photo remediation, biopesticides, bio-fertilizers; Bio-reactors- Design and application 2 L1, L2, L3	2
Renewable and Non-renewable Resources, Forests, water, minerals, Food and land (with example of one case study); Energy, Growing energy needs, energy sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity.311, L23Environmental Pollution Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution- Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management.34Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizer; Bio-reactors- Design and application2L1, L2, L3	2
Iand (with example of one case study); Energy, Growing energy needs, energy sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity. 3 L1, L2 3 Environmental Pollution 3 L1, L2 3 Environmental Pollution 3 1 3 Environmental Pollution 3 3 4 Environmental Biotechnology 3 1 1 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, biosensors; Remedial measures- Bio-remediation, photo remediation, biopesticides, bio-fertilizers; Bio-reactors- Design and application 2 L1, L2, L3	2
sources (conventional and alternative), Biodiversity at global, national and local levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity. L1, L2 3 Environmental Pollution Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution-Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. 3 4 Environmental Biotechnology 2 Biotechnology for environmental protection- Biological indicators, biosensors; Remedial measures- Bio-remediation, photo remediation, biopesticides, bio-fertilizer; Bio-reactors- Design and application 2	2
Iocal levels; India as a mega-diversity nation; and strategies for conservation of Biodiversity.India as a mega-diversity nation; and strategies for conservation of Biodiversity.3Environmental Pollution Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution- Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management.3L1, L2, L34Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application2L1, L2, L3	
of Biodiversity. Environmental Pollution Image: Second secon	
3 Environmental Pollution Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution-Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. 3 L1, L2, L3 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio-sensors; Remedial measures- Bio-remediation, photo remediation, bio-pesticides, bio-fertilizers; Bio-reactors- Design and application 2 L1, L2, I	
Types of pollution- Air, water (including urban, rural, marine), soil, noise, thermal, nuclear; Pollution prevention; Management of pollution- Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management.3L1, L2, L34Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application3L1, L2, L3	
thermal, nuclear; Pollution prevention; Management of pollution- Rural/Urban/Industrial waste management [with case study of any one type, e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. L1, L2, L3 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application 2	
Rural/Urban/Industrial waste management [with case study of any one type, EII, E2, E3 e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- 2 L1, L2, I pesticides, bio-fertilizers; Bio-reactors- Design and application	3 1 4
e.g., power (thermal/nuclear), fertilizer, tannin, leather, chemical, sugar], Solid/Liquid waste management, disaster management. 4 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application 2 L1, L2, I	<i>'</i> , D I
Solid/Liquid waste management, disaster management. 4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application 2	
4 Environmental Biotechnology Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application	
Biotechnology for environmental protection- Biological indicators, bio- sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application2L1, L2, I	
sensors; Remedial measures- Bio-remediation, photo remediation, bio- pesticides, bio-fertilizers; Bio-reactors- Design and application	13
pesticides, bio-fertilizers; Bio-reactors- Design and application	L3
5 Social Issues and Environment	
Problems relating to urban environment- Population pressure, water scarcity,	
industrialization; remedial measures; Climate change- Reasons, effects (global ⁵ L1, L2, L3	3. L4
warming, ozone layer depletion, acid rain) with one case study; Legal issues-	,
Environmental legislation (Acts and issues involved), Environmental ethics	
6 Environmental Monitoring	
Monitoring- Identification of environmental problem, tools for monitoring	
(remote sensing, GIS): Sampling strategies- Air, water, soil sampling 2 L1, L2, L3	5, L4
techniques	
TotalHours 15	



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Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Textbook of Environmental Studies for Undergraduate Courses	ErachBharucha	University's Press	Second Edition	2013
2	Fundamentals of Environmental Studies	MahuaBasu& Xavier Savarimuthu SJ	Cambridge Publication	First Edition	2016
3	Environmental Studies	Benny Joseph	Tata McGraw – Hill Publishing Company Limited	First Edition	2015
4	Environmental Studies	R.J.Ranjit Daniels, JagadishKrishnaswamy	Wiley India Private Ltd., New Delhi.	First Edition	2013
5	Introduction to Environmental Engineering and Science	Gilbert M.Masters	Pearson- Education	Third Edition	2008

Online References:

Sr. No.	Website Name	URL	Modules Covered
1.	www.conserve-energy-	https://www.conserve-energy-future.com/what-is-	M1
	future.com	environmental-science-and-its-components.php	
2.	www.vikaspedia.in/InDG	http://vikaspedia.in/energy/environment/biodiversity-	M2
		1/conservation-of-biodiversity	
3.	www.encyclopedia.com	https://www.encyclopedia.com/earth-and-	M3
		environment/ecology-and-environmentalism/environmental-	
		studies/environmental-toxicology	
4.	www.environmentalscience.	https://www.environmentalscience.org/career/environmental-	M4
	org	biotechnology	
5.	www.forestresearch.gov.uk	https://www.forestresearch.gov.uk/tools-and-resources/urban-	M5
		regeneration-and-greenspace-partnership/greenspace-in-	
		practice/practical-considerations-and-challenges-to-	
		greenspace/social-and-environmental-justice/	
6.	www.unece.org/info/ece-	https://www.unece.org/environmental-policy/environmental-	M6
	homepage.html	monitoring-and-assessment/areas-of-work/environmental-	
		monitoring.html	

List of Practical/ Experiments:NA



B.E. (Computer Engineering)				S.E.	SEM : III				
Course Name :Professional Skills-III(Basic Technology Skills) (Object Oriented Programming using Java)					Course Cod	e :HSD-CSPS	301		
Teaching Scheme (Program Specific) E				Exa	minati	on Scheme (Form	ative/ Summ	ative)	
Mode	s of Teach	ing / Learn	ing / Wei	ghtage	Мо	Modes of Continuous Assessment / Evaluation			
Hours Per Week				Theory (100)		Presentation (25)	Report (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	(AC)	(AC)	
1	-	2	3	2	-	-	50	25	75
		IA: In-S	emester A	ssessmen	t - Pap	er Dur	ation – 1.5 Hours		
	ESE: End Semester Examination - Paper Duration - 3 Hours								
The we	AC: Activity The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)								

Prerequisite: Computer Basics, Procedural Programming Languages

Course Objective: The course intends to deliver the OOP concepts using Java and to help students design and implement real world applications.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Apply fundamental programming constructs.	L1, L2, L3
2	Experiment with concept of class, objects, strings, arrays and vectors.	L1, L2, L3
3	Experiment with concept of inheritance and interfaces.	L1, L2, L3
4	Experiment with concept of exception handling.	L1, L2, L3
5	Experiment with notion of multithreading and packages.	L1, L2, L3
6	Develop GUI based application	L1, L2, L3

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Object Oriented Concepts		L1, L2, L3
	Basics of object oriented programming , OOP Concepts: Object,		
	Class, Encapsulation, Abstraction, Inheritance, Polymorphism	2	
2	Introduction to Java		L1, L2, L3
	Basics of Java programming, Data types, Variables, Operators,	3	
	Looping ,Strings, Arrays in java ,Input / Output in java , objects and		
	classes in java, , Constructor and its types, Visibility modifiers, this		
	reference		



	Bilder TGET Autonomy Scheme - 2013		
3	Inheritance and Polymorphism		L1, L2, L3
	Inheritance in java, Super and sub class, Polymorphism, Dynamic	2	
	binding, Abstract class, Interface in java		
4	Exception Handling		L1, L2, L3
	Exception and Error, Use of try, catch, throw, throws and finally,		
	Built in Exception, Custom exception, Throwable Class	2	
5	Multithreading in java		L1, L2, L3
	Thread life cycle and methods, Thread class, Runnable interface,		
	Thread synchronization.Package in java	2	
6	Event and GUI programming		L1, L2, L3
	Event handling in java, Event types, Mouse and key events, GUI		
	Basics, Panels, Frames, Layout Managers: Flow Layout, Border	4	
	Layout, Grid Layout, GUI components like Buttons, Check Boxes,		
	Radio Buttons, Labels, TextFields, Text Areas, Combo Boxes, Lists,		
	Scroll Bars, Sliders, Windows, Menus, Dialog Box, Applet and its life		
	cycle, Introduction to swing Database Connectivity		
	TotalHours	15	

Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Programming with Java(Fifth Edition)	E Balagurusamy	McGraw Hill	Sixth Edition	2019
2	Java Programming, D. S. Malik	D. S. Malik	Cengage Learning	First Edition	2009
3	Programming in Java	Sachin Malhotra &Saurabh Chaudhary	Oxford University Press	Second Edition	2018
4	The Complete	Herbert Schild	McGraw Hill.	Fourth	
	Reference, Java 2			Edition	2011
5	Head First Java: A Brain-Friendly Guide	Kathy Sierra and Bert	O'Reilly Media	Second Edition	2005

Online References:

Sr.	Website Name	URL	Modules
No.			Covered
1	www.javatpoint.com	https://www.javatpoint.com/java-oops-concepts	M1,M2,M3
2	www.w3schools.com	https://www.w3schools.com/java/	M1-M6
3	www.programiz.com	https://www.programiz.com/java-programming	M1-M6
T • 4 6			

List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	RBT Levels
1	Basic Experiments	Apply installation steps to set the environment variables and run a simple java program.	2	L1, L2



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2		Experiment with various ways to accept data through keyboard for 1D	2	L1, L2
		and 2D array		
	Design Experiments	Experiment with class creation		L1, L2, L3
3		including members and methods,	2	
		accepting and displaying details for		
		single object.		
4		Experiment with constructor and	2	L1, L2, L3
4		Constructor overloading	2	
5		StringBuffer	Z	L1, L2, L3
6		Experiment withsingle and multilevel	2	L1, L2, L3
		inheritance (Use super keyword).		
		Experiment withdemonstration of try,		L1, L2, L3
7		catch, throw, throws and finally	2	
8		Experiment withcreating user defined	2	L1, L2, L3
		package	2	
9		Experiment with implementing	2	L1, L2, L3
		multithreading using Thread class and		
10		Runnable interface	2	
10		demonstrate Craphics Font and	Z	L1, L2, L3
		Color class		
11		Experiment withcreation of GUI	2	111213
11		application with event handling using	2	L1, L2, L3
		AWT controls		
12		Make use of database connectivity to	2	L1. L2. L3
		develop java application.		, , , -
13	Mini/Minor	Mini Project based on content of the	6	L1, L2, L3
	Projects/ Seminar/	syllabus. (Group of 2-3 students)		
	Case Studies	1. Gaming System		
		2. Hotel Reservation System		
		3. Airline Reservation System		
		4. Hospital Management System		
		5. Online chat application		
		6. E-commerce website		
		TotalHours	30	



B.E. (Computer Engineering)					S.E. SEM	[: III	
Course Name : Project Based Learning -				l Learning	g – I	Course Code :HS	D-CSABL301
Teaching scheme (Holistic Student Development - HSD) Industry Specific/Interdisciplinary				ent ry	Examination Scheme (Formative/ Summative)		
Mode	Modes of Teaching / Learning / Weightage				Assessment/Evaluation Scheme		
Hours Per Week					Presentation (25)	Report (25)	Term Work
Theory	Tutorial	Practical	Contact Hours	Credits	(AC)	(AC)	
-	-	2	2	1	25	-	25
		IA: In-S	emester A	ssessmen	t - Paper Dura	tion – 1.5 Hours	
	ESE: End Semester Examination - Paper Duration - 3 Hours						
The we	The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)						
Prerequ	isite: Com	puter Fund	amentals a	nd Knowl	edge of Program	ming Languages	

<u>Course Objective</u>: The Course intends to aid students identify real world problems and apply computing fundamental and technical skill to find solutions to them.

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Interpret the basic real time problems.	L1, L2
2	Apply appropriate technologies and programming constructs to solve problems.	L1, L2, L3
3	Inspect the results obtained for documentation and presentation.	L1, L2, L3, L4

<u>Course Outcomes:</u>Upon completion of the course students will be able to:



Projects Listing:

Sr. No.	Title of Project	Type of Project
1	Implementing system for text encryption and decryption	Application
2	Implementing Hospital Management System	Application
3	Implementing Employee Management System	Application
4	Implementation of Payroll System	Application
5	Implementing system for Bus Booking	Application
6	Implementation of Currency Converter System	Application
7	Design and Development of Game	Core
8	Design and Development of system for scheduling of events	Core
9	Design and development of IQ Test System	Core
10	Develop an app for Invoice	Core



B.E.(ALL BRANCHES)					S.E. S	SEM : III			
Course Name : Activity Based Learning-III					Course Code	HSD-CSAB	L301		
Teaching Scheme (Program Specific)				Exa	Examination Scheme (Formative/ Summative)				
Modes of Teaching / Learning / Weightage				Modes of Continuous Assessment / Evaluation				ation	
Hours Per Week				Theory (25)		Presentation (25)	Report (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	(AC)	(AC)	
-	-	2	2	1	-	-	25	25	50
IA: In-Semester Assessment - Paper Duration – 1.5 Hours									
ESE: End Semester Examination - Paper Duration - 3 Hours									
	• 1 / 0	1.0		AC:	Activit	y			T . 1

The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance/Learning Attitude (20%)

Prerequisite: Basics of Computer Programming, General knowledge, Social awareness

<u>Course Objective</u>: The larger objective of the course is to develop the Socially Sensitive Citizens by creating awareness among students through Activity mode.

The course intends to deliver the understanding of the concepts by encouraging the students to look beyond their textual knowledge, establish the relationship between theory and the applications of the learned concepts. It also intends to address the social issues and create awareness.

<u>Course Outcomes:</u>Upon completion of the course students will be able to:

Sr. No.	Course Outcome	Cognitive levels of attainment as per Bloom's Taxonomy
1	Construct his views independently and demonstrate various debate styles.	L1, L2, L3
2	Identify the various benefits of quiz competitions.	L1, L2, L3
3	Utilize the society awareness in various social issues	L1, L2, L3



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Module	Topics	Hrs.	Cognitive levels of
No.			attainment as per
1	Eutoma aus /Dahata		Bloom's laxonomy
1	Litempore/Debate		L1, L2, L3
	Brainstorming session among students on various topics floated for	4	
	debate Topics can be Academic or Parliamentary Financial	•	
	International affairs, technology trends, Technical or philosophical.		
	Extempore speech by each student for /against topic for 1 minute.		
	II. Debate competition . Formation of four teams for two topics.		
	Two teams (For and against) for topic I will debate first and the other		
	two team will be audience and for topic II vice-versa.		
	Evaluation by faculty as per format.		
2	General Knowledge (Technical and Current Affairs)		L1, L2, L3
	I. Introduction to Quiz, Definition, Types of quiz, Rules of quiz, quiz	4	
	rounds. Quiz competition on Technical topic with 50 MCQ.		
	II . Puzzle/ Quiz competition on current affairs with 50 MCQ.		
	Evaluation by faculty as per format.		
3	Personality Development	4	L1, L2, L3
	1. Word association (Test Sentence Building) (2 Hrs.) Students are	4	
	snown 60 English words one after other and a short sentence using the		
	and sentence is to be written within this period only. At least 45 words		
	are to be attempted to get good marks		
	are to be attempted to get good marks		
	II. Thematic Apperception Test (Short Story Writing)(2 Hrs.)		
	12 Slides will be projected, and stories are to be written in 03 Minutes.		
	Discussions on Stories written by students		
	Evaluation by faculty as per format.		
4	Extended Work		L1, L2, L3
	Introduction to Street play-Types of Street play, Writing and	-	
	demonstration of street Play on social Issues	6	
	Water conservation		
	Waste Management		
	Figure Dati etc. Evaluation by faculty as per format		
5	Awareness creationon social issues		
5	Students will develop material like placard posters etc. for creating		11, 12, 15
	awareness on issue like	6	
	Education on social Issues like social modia, youth related		
	• Education on social issues like social media, youth related		
	Education on health issues		
	 Education on issues related to senior citizen etc. 		
	The education/ awareness needs to be conducted in campus through		
	presentation(placards, posters etc.).		
	Evaluation by faculty as per format		
	Data collection and Analysis:		
	survey's needs to be developed and conducted, data analysis and		
	results interpretation		



	Evaluation by faculty as per format		
6	Extempore/Debate		L1, L2, L3
	I. Introduction to debate, Definition and types of Debate		
	Brainstorming session among students on various topics floated for	6	
	debate. Topics can be Academic or Parliamentary, Financial,		
	International affairs, technology trends, Technical or philosophical.		
	Extempore speech by each student for /against topic for 1 minute.		
	II. Debate competition. Formation of four teams for two topics. Two		
	teams (For and against) for topic I will debate first and the other		
	two team will be audience and for topic II vice-versa.		
	Evaluation by faculty as per format.		
	TotalHours	30	

Books and References:

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	Competitive Debate	Richard Earl	Alpha	-	2008
2	Times Quiz bookby Times Mind	OLovBjortomt	Times Books	-	2016
	Games				
3	Cracking the codingInterview	GayleLaakmann	Createspace	-	2011

Online References:

Sr. No.	Website Name	URL	Modules Covered
1	www.cleverism.com	https://www.cleverism.com/18-best-idea-generation-techniques/	M1
2	www.thebetterindia.com	https://www.thebetterindia.com/111/teaching-street- children-a-thing-or-two/	M6