

<b>B.E.</b> (Computer Engineering)						T.E.	SEM: V		
Course Name: Theory of Computer Science						Course Code: PCC-CS501			
Teaching Scheme (Program Specific) Examin					aminati	ion Scheme (Form	ative/ Summat	ive)	
Mode	es of Teach	ing / Learn	ing / Weig	htage	M	odes of	Continuous Assess	sment / Evalua	tion
	Ho	ours Per Week		′eek		Theory Practical/Oral Term To (100) (25) Work (25)			
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	1	-	4	4	25	75	-	25	125
		IA: In-	Semester A	Assessmen	ıt - Pap	er Dura	tion – 1.5 Hours		
ESE: End Semester Examination - Paper Duration - 3 Hours									
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely									
D	C	ompletion o	t practical	(40%) and	Attenda	ance / Lo	earning Attitude (20	J%)	
Prerequ	isite: Basic	Mathematic	CS						

**<u>Course Objective:</u>** The Objective of this course is to deliver the fundamental concepts of theory of computation describing formal mathematical models of computation such as FA,PDA,LBA and TM by comparing their power, limitations, languages and their applications in computation and complexity theory and also to learn that not all problems are solvable by computers.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Describe formal models of computation, such as finite automata, pushdown automata, and Turing machines.	L1, L2, L3
2	Design Finite Automata's for different Regular Expressions and Languages.	L1, L2, L3
3	Compare different types of Grammars and design context free grammars for formal languages.	L1, L2, L3
4	Construct and analyze Push Down automata and Turing Machine for formal languages.	L1, L2, L3,L4
5	Classify machines by their power to recognize languages.	L1, L2, L3,L4
6	Express the understanding of the decidability and decidability problems.	L1,L2

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	<b>Basic Concepts and Finite Automata</b>		
	Basic Concepts: Alphabets, Strings, Languages, Closure properties.		
	FA without output: Finite Automata (FA), Deterministic Finite Automata	9	L1, L2, L3
	(DFA) and Nondeterministic Finite Automata (NFA): Definitions, transition		
	diagrams and Language recognizers, NFA to DFA Conversion,		

TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	Etd. in 2001
Equivalence of Deterministic and Nondeterministic Finite Automata,	

lotal Hours	45	
Rice's Theorem , Post's correspondence problem (PCP).		
and Undecidability, Halting problem, Linear bounded automata (LBA),	6	
Properties of recursive and recursively enumerable languages, Decidability		L1, L2
Undecidability		
and Multi Tape TMs. Applications, Power and Limitations of TMs.		
Variants of TM: Multitrack, Multitape, Universal TM, Equivalence of Single	10	, , ,
Definition, Transitions, Design of TM as generator, decider and acceptor.		L1, L2, L3,L4
Turing Machine		
Deterministic PDA, Equivalence of CFG and PDA, Application of PDA		
Acceptance by Empty stack and its Equivalence. Deterministic PDA Non-	,	£1, £2,£3,£4
Definition Model Acceptance of CFL Acceptance by Final State and	7	L1. L2.L3.L4
Push Down Automete		
Normal Forms (CNF) and Greibach Normal Forms (GNF), CFLs - Pumping		
Ambiguity. Simplification and Applications. Normal Forms: Chomsky	/	L1,L2, L3
Definition, Sentential forms, Leftmost and Rightmost derivations, Parse tree,	7	1112 12
Context Free Grammar		
lemma and Closure properties of regular languages.		
Regular Language (RL): Proving languages to be Nonregular -Pumping		
RE Applications. Grammars and Chomsky hierarchy.		
Regular Expression (RE): Equivalence of RE and FA, Arden's Theorem,	6	L1,L2, L3
Regular Expressions and Languages		
and Equivalence ,Applications and limitations of FA.		
<b>FA with output:</b> Definition and construction of Moore and Mealy machines		
Eliminating Ensilon transitions Minimization of DFA		
Applications of Finite Automata Finite Automata with Ensilon Transitions		
	Applications of Finite Automata, Finite Automata with Epsilon Transitions, Eliminating Epsilon transitions, Minimization of DFA.         FA with output: Definition and construction of Moore and Mealy machines and Equivalence ,Applications and limitations of FA.         Regular Expression (RE): Equivalence of RE and FA, Arden's Theorem, RE Applications. Grammars and Chomsky hierarchy.         Regular Language (RL): Proving languages to be Nonregular -Pumping lemma and Closure properties of regular languages.         Context Free Grammar         Definition, Sentential forms, Leftmost and Rightmost derivations, Parse tree, Ambiguity. Simplification and Applications. Normal Forms: Chomsky Normal Forms (CNF) and Greibach Normal Forms (GNF), CFLs - Pumping lemma, Closure properties         Push Down Automata         Definition, Model, Acceptance of CFL, Acceptance by Final State and Acceptance by Empty stack and its Equivalence, Deterministic PDA , Non-Deterministic PDA , Equivalence of CFG and PDA, Application of PDA         Turing Machine         Definition, Transitions, Design of TM as generator, decider and acceptor. Variants of TM: Multitrack, Multitape , Universal TM, Equivalence of Single and Multi Tape TMs. Applications, Power and Limitations of TMs.         Undecidability         Properties of recursive and recursively enumerable languages, Decidability and Undecidability , Halting problem , Linear bounded automata (LBA), Rice's Theorem , Post's correspondence problem (PCP).	Applications of Finite Automata, Finite Automata with Epsilon Transitions, Eliminating Epsilon transitions, Minimization of DFA.         FA with output: Definition and construction of Moore and Mealy machines and Equivalence , Applications and limitations of FA.         Regular Expression (RE): Equivalence of RE and FA, Arden's Theorem, RE Applications. Grammars and Chomsky hierarchy.       6         Regular Language (RL): Proving languages to be Nonregular -Pumping lemma and Closure properties of regular languages.       6         Context Free Grammar       7         Definition, Sentential forms, Leftmost and Rightmost derivations, Parse tree, Ambiguity. Simplification and Applications. Normal Forms: Chomsky Normal Forms (CNF) and Greibach Normal Forms (GNF), CFLs - Pumping lemma, Closure properties       7         Push Down Automata       7         Definition, Model, Acceptance of CFG and PDA, Application of PDA       7         Macceptance by Empty stack and its Equivalence, Deterministic PDA , Non-Deterministic PDA , Equivalence of CFG and PDA, Application of PDA       10         and Multi Tape TMs. Applications, Power and Limitations of TMs.       10         and Multi Tape TMs. Applications, Power and Limitations of TMs.       6         Properties of recursive and recursively enumerable languages, Decidability and Undecidability , Halting problem , Linear bounded automata (LBA), Rice's Theorem , Post's correspondence problem (PCP).       6

SN	Title	Authors	Publisher	Edition	Year
1	Introduction to	John. E. Hopcroft,	Pearson Education	3rd Edition	2006
	Automata Theory,	Rajeev otwani, J. D.	Asia		
	Languages and	Ullman,			
	Computation				
2	Elements of the	H.R. Lewis and	Prentice Hall Inc	2nd Edition	1997
	Theory of	C.H.Papadimitrou			
	computation				
3	Introduction to	John C Martin	TMH	4th Edition	2010
	languages and the				
	Theory of				
	Computation				
4	Introduction to	Daniel I.A. Cohen	John Wiley	2nd Edition	2007
	Computer Theory				

# **Online References:**

S.	Website Name	URL	Modules
No.			Covered
1	www.coursera.o rg	https://www.coursera.org/learn/cs-algorithms-theory- machines	M6
2	nptel.ac.in	https://nptel.ac.in/noc/individual_course.php?id=noc16-cs14	M1-M6



# TCET

DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019

ice

Estd. in 2001



Sr.	Торіс	Hrs.	Cognitive levels of
No.			attainment as per Bloom's
			Taxonomy
1	Tutorial on Construction of Finite Automata.	1	L1, L2, L3
2	Tutorial on Regular Expression.	1	L1, L2, L3
3	Tutorial on Regular Expression to Non-Deterministic	1	L1, L2, L3
	Finite Automata.		
4	Tutorial on Conversion of NFA to DFA.	1	L1, L2, L3
5	Tutorial on Construction of Mealy and Moore Machine.	1	L1, L2, L3
6	Tutorial on Construction of CFG and Derivations.	1	L1, L2, L3
7	Tutorial on Simplification of Context Free Grammar.	1	L1, L2, L3
8	Tutorial on Conversion of CFG into Normal Forms	1	L1, L2, L3
	(CNF & GNF).		
9	Tutorial on Construction of PDA.(I)	1	L1, L2, L3
10	Tutorial on Construction of PDA.(II)	1	L1, L2, L3
11	Tutorial on Application of Pumping Lemma.	1	L1, L2, L3
12	Tutorial on Conversion of CFG to PDA.	1	L1, L2, L3
13	Tutorial on Construction of Turing Machine.(I)	1	L1, L2, L3, L4
14	Tutorial on Construction of Turing Machine (II)	1	L1, L2, L3, L4
15	Tutorial on Post Correspondence Problem.	1	L1, L2
	Total Hours	15	



B.E. (Computer Engineering)					T.E. SEM: V				
Course Name: Introduction to Intelligent System					Course Code: PCC-CS502				
Teaching Scheme (Program Specific)				Ex	aminati	ion Scheme (Form	ative/ Summat	ive)	
Mod	es of Teach	ing / Learn	ing / Weig	htage	M	odes of	Continuous Assess	sment / Evalua	tion
Hours Per Week				The (1	eory 00)	Practical/Oral (25)	Term Work (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2	5	4	25	75	25	25	150
		IA: In-	Semester A	Assessmer	nt - Pap	er Dura	tion – 1.5 Hours		
	ESE: End Semester Examination - Paper Duration - 3 Hours								
The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely									
	С	ompletion o	of practical	(40%) and	Attend	ance / L	earning Attitude (2	0%)	
Prerequ	isite: Comp	outer and Pro	ogramming	Basics					

**<u>Course Objective:</u>** To make students understand and explore the techniques underlying the design of Intelligent Systems.

<u>Course Outcomes: Upor</u>	completion of the course	students will be able to:
------------------------------	--------------------------	---------------------------

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's
		Taxonomy
1	Understand different types of AI agents.	L1, L2
2	Understand AI building blocks presented in intelligent agents.	L1, L2
3	Understand and Apply various AI search algorithms uninformed, informed, local, adversarial and backtracking search algorithms to real- world problems.	L1, L2, L3
3	Analyze AI approaches for knowledge representation and Uncertain knowledge and reasoning.	L1, L2, L3
4	Understand and apply methods for solving Constraint Satisfaction Problems.	L1, L2, L3
5	Understand various types of planning and forms of learning. Apply decision tree learning to a given problems.	L1, L2, L3
6	Understand various sub areas of Intelligent Systems.	L1, L2

Modul e No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction	4	L1, L2
	Introduction, History of Artificial Intelligence, Intelligent System		
	Categorization, Components of AI, Foundations of AI, Applications of AI,		
	Current trends in AI.		
2	Intelligent Agents	4	L1, L2



	Agents and Environments, The concept of rationality, The nature of		
	environment, The structure of Agents, Types of Agents, Learning Agent.		
3	Problem Solving and Search	10	L1, L2, L3
	Problem Solving Agent, Formulating Problems, Example Problems,		
	Uninformed Search Methods, Informed Search Method, Local Search		
	Methods, Genetic algorithms, Adversarial Search, Constraint Satisfaction		
	Problems		
4	Knowledge and Reasoning		L1, L2, L3
	Knowledge based Agents, The Wumpus World, The Propositional logic,	10	
	First Order Logic: Syntax and Semantic, Inference in FOL, Forward		
	chaining, backward Chaining, Knowledge Engineering in First-Order		
	Logic, Unification, Resolution, Uncertain knowledge and reasoning:		
	Uncertainty, Representing knowledge in an uncertain domain, The		
	semantics of belief network, Inference in belief network		
5	Planning and Learning		L1, L2, L3
	The planning problem, Planning with state space search, Partial order		
	planning, Hierarchical planning, Conditional Planning. Learning: Forms of	9	
	Learning, Inductive Learning, Learning Decision Tree, Expert System:		
	Introduction, Phases in building Expert Systems, ES Architecture, ES vs		
	Traditional System		
6	Sub Areas of Intelligent Systems		L1, L2
	Artificial Neural Network, Fuzzy Systems, Natural Language Processing,	8	
	Robotics		
	Total Hours	45	

	Title	Authors	Publisher	Edition	Year
1	Artificial Intelligence	Stuart J. Russell and Peter Norvig	McGraw Hill	3rd	
	a Modern Approach			Edition	2009
2	A First Course in	Deepak Khemani	McGraw Hill	1 <sup>st</sup>	
	Artificial Intelligence		Education	Edition	2013
			(India)		
3	Artificial Intelligence	N. P. Padhy	Oxford	1 st	
	and Intelligent			Edition	2005
	Systems				
4	Artificial Intelligence	Elaine Rich and Kevin Knight	Tata McGraw-	3 <sup>rd</sup>	
			Hill Education	Edition	2008
			Pvt. Ltd.		
5	Artificial Intelligence	Rob Callan	Palgrave	1 <sup>st</sup>	2003
			macmillan	Edition	

#### **Online Resources:**

S. No.	Website Name	URL	Modules Covered
1	nptel.ac.in	https://nptel.ac.in/courses/106102220/	M1-M6



**TCET** 

DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019



# **List of Practical/ Experiments:**

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	RBT Levels
1	Basic Experiments	Specify problem formulation for an AI problem and Implement the same.	2	L1, L2
2		Apply uninformed search on given problem.	2	L1, L2, L3
3		Apply informed search on given problem.	2	L1, L2, L3
4	Design Experiments	Apply Adversarial Search on given problem.	2	L1, L2, L3
5		Apply genetic algorithm on given problem.	2	L1, L2, L3
6		Apply Minimax with Alpha-Beta Pruning on given problem.	2	L1, L2, L3
7		Apply Backtracking Search on given problem.	2	L1, L2, L3
8		Solve a reasoning problem using unification.	2	L1, L2, L3
9		Apply Decision Tree Learning on given problem.	2	L1, L2, L3
10	Case Studies and Mini Project	Game Development Smart Apps Chatbot Prediction Systems Intelligent Systems	12	L1, L2, L3,L4,L5, L6
		Total Hours	30	



B.E. (Computer Engineering)					T.E	. SEM: V			
Course Name: Software Engineering				Course C	Code: PCC-CS503				
Teaching Scheme (Program Specific)         Examination Scheme (Formative/ Summation)					ive)				
Mod	es of Teach	ing / Learn	ing / Weig	htage	Μ	odes of	Continuous Asses	sment / Evalua	tion
	Но	ours Per We	ek		Th (1	eory 00)	Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2	5	4	25	75	25	25	150
	IA: In-Semester Assessment - Paper Duration – 1.5 Hours								
ESE: End Semester Examination - Paper Duration - 3 Hours									
The	The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance / Learning Attitude (20%)								
Prerequ	isite: Objec	t Oriented F	Programmi	ng, Frontei	nd Back	end com	nectivity		

**Course Objective:** The objective of the course is to introduce to the students about the development of software product, the processes that provides a framework for the engineering methodologies and practices. Also to give the information regarding the phases including the analysis, design, testing methodologies and quality assurance.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Understand the use of basic and advanced models in software engineering	L1, L2
2	Analyze the scenarios to design the UML diagrams	L1, L2, L3,L4
3	Understand and apply the different techniques of project estimation an understand the tracking methods	L1, L2, L3,L4
4	Understand the design concepts and apply them to the project	L1, L2, L3,L4
5	Identify risks, manage the change to assure quality in software project.	L1, L2, L3,L4
6	Apply the principles of testing and develop test plan for the project	L1, L2, L3,L4

Modul e No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction		L1, L2,L3
	Introduction to software engineering, Importance of Software engineering		
	Software Process, Various models for Software Development (Waterfall,	6	
	Spiral, Agile (Scrum), V-Model, RAD, DevOps), Capability Maturity		
	Model (CMM).		



	onder roch Autonomy Scheme - 2013		
2	<b>Requirements Analysis and Modelling</b>		L1, L2, L3,L4
	Requirement Elicitation, Software requirement specification (SRS), Data		
	Flow Diagram(DFD), Feasibility Analysis, Cost- Benefit Analysis,	8	
	Developing Use Cases (UML), Requirement Model - Scenario-based		
	model, Class-based model, Behavioral model.		
3	Project Scheduling and Tracking		L1, L2, L3,L4
	Software Project Estimation: LOC, FP, Empirical Estimation Models -		
	COCOMO II Model	4	
	Project scheduling: Timeline charts, CPM		
4	Software Design	8	L1, L2, L3
	Design Concepts, Characteristics of Good Design, Effective Modular		
	Design – Cohesion and Coupling. Architectural Styles, UI Design		
5	Software Risk, Configuration Management & Quality Assurance	8	L1, L2, L3,L4
	Risk Identification, Risk Assessment, Risk Projection, RMMM, Software		
	Configuration management, Software Quality Assurance: Software		
	Reliability, Formal Technical Review (FTR), Walkthrough		
6	Software Testing and Maintenance	11	L1, L2, L3,L4
	Software Testing, Unit testing, Integration testing Verification, Validation		
	Testing, System Testing, Test plan, White-Box Testing, Basis Path		
	Testing, Control Structure Testing, Black-Box Testing, Software		
	maintenance and its types, Software Re-engineering, Reverse Engineering		
	Total Hours	45	

	Title	Authors	Publisher	Edition	Year
1	Software Engineering: A Practitioner's Approach"	Roger Pressman	McGraw-Hill Publications	Sixth Edition	2009
2	Software Engineering	Ian Sommerville	Pearson Education	9th Edition	2017
3	Software Engineering Fundamentals	Ali Behfrooz and Fredeick J.Hudson,	Oxford University Press	1st edition	1997
4	Software Engineering – Concepts and Practices	Ugrasen Suman	Cengage Learning	1st edition	2012
5	An integrated approach to Software Engineering	Pankaj Jalote	Springer/Narosa	1st edition	2012

# **Online Resources:**

S.	Website Name	URL	<b>Modules</b> Covered
No.			
1	www.tutorialspoint	https://www.tutorialspoint.com/sdlc/sdlc_overview.htm	M1-M6
	.com		
2	www.guru99.com	https://www.guru99.com/software-testing-introduction-	M1-M3,
		importance.html	
3	www.tutorialspoint	https://www.tutorialspoint.com/software_testing/software	M4,M6
	.com	_testing_qa_qc_testing.htm	



4	https://en.wikipedi	https://en.wikipedia.org/wiki/DevOps	M1
	a.org		

# List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	RBT Levels
1	Basic Experiments	Apply the knowledge of SRS and prepare Software Requirement Specification (SRS) document in IEEE format for the project	2	L1, L2, L3
2		Sketch a DFD (up to 2 levels)	2	L1, L3
3		Sketch UML Use case Diagram for the project.	2	L1, L3
4		Sketch a Class Diagram for the project.	4	L1, L3
5		Sketch Activity, State Transition diagram for the project.	4	L1, L3
6		Sketch Sequence and Collaboration diagram for the project	4	L1, L3
7	Design Experiments	Use project management tool to prepare schedule for the project.	2	L1, L3
8		Change specification and use any SCM Tool to make different versions	2	L1, L3
9		Design test cases and generate test scripts in Selenium	4	L1, L2, L3
10	Mini/Minor Projects/ Seminar/ Case Studies	Mini Project:         1. Online banking system         2. Online hotel management system         3. Online sales Order Processing and Invoicing	4	L1, L2, L3, L4
		TotalHours	30	



# **TCET** DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019

Ice

Estd. in 2001

#### T.E. Semester –V

	В	.E. (Compu	ter Engine	eering)			T.E	. SEM: V	
	(	Course Nam	e: Micropro	ocessor			Course C	ode: PCC-CS504	ŀ
Те	aching Sch	neme (Prog	ram Specif	iic)	Ex	aminati	ion Scheme (Form	ative/ Summat	ive)
Modes of Teaching / Learning / Weightage					age Modes of Continuous Assessment / Evaluation				tion
	Ho	ours Per We	ek		The (1	Theory (100)Practical/Oral (25)Term Work (25)Total			
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2	5	4	25	75	25	25	150
		IA: In-	Semester 2	Assessmer	nt - Pap	er Dura	tion – 1.5 Hours		
		ESE: En	d Semeste	r Examina	ation -	Paper I	<b>Duration - 3 Hours</b>	1	
The	weightage (	of marks for completion o	<b>r continuo</b> f practical	us evaluat (40%) and	tion of ' Attend	<b>Гегт w</b> ance / L	ork/Report: Forma earning Attitude (2	ative (40%), Tir 0%)	nely
Prerequ	isite: Basic	Mathematic	s	·					

Course Objective: The course intends to introduce basic and advanced software and hardware architecture of Intel X86 processors, use of assembly language and mixed mode programming. It also introduces microcontroller and its applications.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Describe 16-bit architecture of 8086 Microprocessor.	L1, L2
2	Apply the assembly and mixed language programming to develop small embedded application.	L1, L2, L3
3	Sketch 8086 based system using memory and peripheral chips.	L1,L2, L3
4	Analyze the role of 32bit microprocessor architecture over 16 bit architecture.	L1,L2,L3,L4
5	Compare Pentium family microprocessors.	L1, L2, L3,L4
6	Differentiate between microprocessor and microcontroller.	L1, L2,L3,L4

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Intel 8086 Microprocessor Architecture of 8086 processor, Register set, Memory segmentation, Functional Pin Diagram, Operating Modes, Minimum mode 8086 system and Timing diagrams, Maximum mode 8086 system and Timing diagrams.	8	L1, L2

s tcet	TCET	R
	DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019)	
	Under TCET Autonomy Scheme - 2019	Estd. in 2001

2	Instruction set and Assembly Language Programming		
	Instruction set, Addressing Modes, Assembler Directives, Macros and	6	L1, L2, L3
	Procedure, Assembly Language Programming, Mixed Mode		
	programming		
3	Memory and Peripheral Interfacing with 8086		
	Memory Interfacing - RAM and ROM		
	8259 PIC – Interrupt, Types of Interrupts, Interrupt Service Routine,	8	L1,L2, L3
	Interrupt Vector Table, Block Diagram of 8259, Interfacing the 8259		
	in single and cascaded mode with 8086.		
	8255 PPI - Block diagram, Command word format, Interfacing 8255		
	with 8086.		
4	Intel 80386DX Processor		
	Architecture of 80386DX processor. Register Organization. Operating	8	L1.L2.L3.L4
	Modes: Real Mode, Protected Mode And Virtual 8086 Mode,		, , , ,
	Protected mode Address Translation mechanism: Segmentation and		
	Paging.		
5	Pentium Family processors		
	Superscalar architecture, Super pipelining, Data flow architecture,		L1, L2, L3,L4
	Comparative study of Pentium family processors.	7	
6	The Microcontroller 8051		
	Introduction to 8051 Microcontroller, Architecture, Pin configuration,		L1, L2,L3,L4
	Memory organization, Input /Output Ports, Serial communication,	8	
	Interrupts		
	Total Hours	45	

SN	Title	Authors	Publisher	Edition	Year
1	8086/8088 family:	John Uffenbeck	PHI	First Edition	2009
	Design Programming				
	and Interfacing				
2	Advanced	K M Bhurchandani, A k	McGraw Hill	Third	2006
	Microprocessors and	Ray		Edition	
	Peripherals				
3	The 80386DX	Walter A Triebel	Prentice Hall	First Edition	1992
	Microprocessor:				
	hardware, Software and				
	Interfacing				
4	Pentium Processor	Tom Shanley & Don	Addison-Wesley	Fourth	2008
	System Architecture	Anderson		Edition	
5	Intel Microprocessors	Barry B. Brey	Pearson Education	Eighth	2009
			India	Edition	
6	Microprocessor and	Douglas Hall	Tata McGraw Hill	Third	2006
	Interfacing			Edition	
	IBM PC Assembly	Peter Abel	PHI	Fifth edition	2002
	language and				
	Programming				
7	The 8051	Mazidi Ali, Muhammad	PHI	Second	2012
	microcontroller and	Mazidi Gillispie Janice		Edition	
	embedded systems				



8	The	8051	Kenneth Ayala J	Thomson	Delmar	Second	1996
	Microcontroller:		5	learning		Edition	
	Architecture,			-			
	Programming,	and					
	Applications						

# **Online References:**

S.	Website Name	URL	Modules
No.			Covered
1	www.datasheets pdf.com	https://datasheetspdf.com/pdf/544568/Intel/8086/1	M1, M2, M4
2	nptel.ac.in	https://nptel.ac.in/courses/106108100/	M1,M2,M3, M4,M5
3	www.alldatashe et.com	https://www.alldatasheet.com/view.jsp?Searchword=80386D &sField=2	M4

# List of Practical/ Experiments:

Practical Number	Type of Experiment	Practical/ Experiment Topic	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Basic	Apply Assembly Language Programing to enter and display 8 bit & 16 bits number	2	L1, L2, L3
2	Experiments	Apply Assembly Language Programing to covert HEX to BCD and BCD to HEX.	2	L1, L2, L3
3	Design	Apply Assembly Language Programing to perform addition and subtraction of two 16 bits numbers using macros and procedure. (Menu Based).	2	L1,L2,L3
4	Experiments	Apply Assembly Language Programing to perform string operations. (i)Accept, (ii) Display, (iii) Concatenation (iv) Compare	2	L1,L2,L3
5		Make use of 8086 Trainer kits in: 1. Hexkey pad Mode 2. Serial Mode	4	L1,L2,L3
6		Illustrate Interfacing on Intel 8086 with 8255- Programmable Peripheral Interface.	2	L1,L2,L3,L4
7	Advanced Experiments	Apply Mixed Language Programing to design a calculator.	2	L1,L2,L3
8		Develop program to interface mouse driver/keyboard/printer drivers.	4	L1,L2,L3,L4
9	Mini/Minor Projects/ Seminar/	<ol> <li>Demonstrate PC-to-PC Communication via RS- 232 Serial Port.</li> <li>Develop an application on Mixed mode programming.</li> <li>Develop an application using Arduino Controller.</li> <li>Develop an application using Raspberry-PI.</li> </ol>	6	L1,L2,L3,L4

SNART E	tcet COMP	DEPARTME [Accredited by Choice Based	TCET TOF COMPUTER ENGINEERING ( NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> Jul Credit Grading System with Holistic Student Development (CBCGS - H 2 Under TCET Autonomy Scheme - 2019	<b>COMF</b> ly 2019] 2019]	P) CEEL Estd. in 2001
	10	Case	1. Compare Multicore processors i3, i5, i7.	4	
		Studies/	2. Compare Von Neumann, Hardwired and Data		L1,L2,L3,L4
		Group	flow architecture		
		Presentation	3 Recent development in hardware components		

**Total Hours** 



	В	.E. (Compu	ter Engine	ering)			T.E	. SEM: V		
Course	Name: Prof	fessional Elec	tive 1(Adva	inced Oper	rating S	ystem)	Course Code: PEC-CS5011			
Teaching Scheme (Program Specific)         Examination					ion Scheme (Form	ative/ Summat	ive)			
Mod	es of Teach	ing / Learn	ing / Weig	htage	Μ	odes of	Continuous Assess	sment / Evalua	tion	
	Ho	ours Per We	eek		The (1	Theory (100)Practical/Oral (25)Term Work (25)Tota				
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW		
3	-	2@	5	4	25	75	25	25	150	
		IA: In-	Semester	Assessmen	nt - Pap	er Dura	tion – 1.5 Hours	<u></u>		
		ESE: En	d Semeste	r Examin	ation -	Paper I	<b>Duration - 3 Hours</b>	ł		
The	weightage o	of marks fo completion c	<b>r continuo</b> of practical	us evalua (40%) and	<b>tion of</b> [ l Attend	Гегт we	ork/Report: Forma earning Attitude (2)	utive (40%), Tin 0%)	nely	
Prerequ	isite: Comp	outer and Pro	ogramming	Basics						

**<u>Course Objective:</u>** To make students understand and explore the techniques underlying the design and implementation of various concepts of advance operating system.

<b>Course Outcomes:</b> Upon completion of the course students will be able to
--------------------------------------------------------------------------------

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's
		Taxonomy
1	Demonstrate understanding of design issues of Advanced operating	L1, L2, L3
	systems and compare different types of operating systems.	
2	Analyse design aspects and data structures used for file subsystem,	L1, L2, L3, L4
	memory subsystem and process subsystem of Unix OS	
3	Demonstrate understanding of different architectures used in	L1, L2, L3, L4
	Multiprocessor OS and analyse the design and data structures used in	
	Multiprocessor operating systems.	
4	Differentiate between threads and processes and compare different	L1, L2, L3, L4
	processor scheduling algorithms used in Multiprocessor OS	
5	Classify Real Time OS and analyse various real time scheduling	L1, L2, L3, L4
	algorithms.	
6	Explore architectures and design issues of Mobile OS, Virtual OS,	L1, L2, L3
	Cloud OS.	

Modul e No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction	4	L1, L2, L3
	Functions of operating systems, Design approaches: layered, kernel based		
	and virtual machine approach, types of advanced operating systems (NOS,		
	DOS, Multiprocessor OS, Mobile OS, RTOS, Cloud OS)		
2	Unix Kernel and File Management	4	L1, L2, L3, L4

	TCET DEPARTMENT OF COMPUTER ENGINEERING (CC (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2 Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	)MP) 019) <sub>3</sub>	Estd. In 2001
	System Structure, User Perspective, Architecture of Unix Operating System, Buffer cache: Header, Buffer Pool, Retrieving, Reading and		
	Writing Buffer, File Representation: inodes: Structure of file Directories,		
	Path conversion to inode, superblock, inode assignment, allocation of disk		
	blocks		
3	Unix Process and Memory management	10	L1, L2, L3, L4
	Detailed design of Process Structure: Kernel Data structures for process,		
	Structure of Uarea and Process table, Process states and Transitions,		
	Context of a Process: Static and Dynamic area of context, Saving the		
	Context Layout of System Memory, Regions, Mapping regions with		
4	Process, page table and mapping virtual address to physical address.		
4	Distributed Operating system concepts	10	L1, L2, L3, L4
	Goals, Distributed Computing Models, Hardware Concepts, Software	10	
	Concepts, Architecture of DOS. Design Issues: Transparency, Flexibility,		
5	Scalability, Reliability, Performance, fault tolerance		
5	Multiprocessor Operating System		L1, L2, L3, L4
	Introduction, Basic multiprocessor system architectures, design issues,	0	
	instruction implementation of the process wait Processor scheduling:	7	
	Issues Co. scheduling Smart scheduling Affinity Based scheduling		
6	Real Time Operating Systems and Mobile OS		111213
0	Characteristics of Real Time operating Systems and Mobile OS	8	$L_1, L_2, L_3$
	Time Operating Systems Scheduling in RTOS: Clock driven cyclic Event	0	
	driven: FDF and rate monotonic scheduling Mobile OS: Architecture		
	Android OS, jOS, Virtual OS, Cloud OS and their design issues		
	Total Hours	45	

	Title	Authors	Publisher	Edition	Year
1	Distributed Systems: Principles and Paradigms	Andrew S. Tanenbaum and Maarten Van Steen	Pearson Education	2nd edition	2016
2	Real-Time Systems: Theory and Practice	Rajib Mall	Pearson Education India	1 <sup>st</sup> Edition	2006
3	Operating System: Internals and Design Principles	William Stallings	Prentice Hall	8th Edition	2014

# **Online Resources:**

S.	Website Name	URL	<b>Modules</b> Covered
No.			
1	https://www.geeks forgeeks.org	https://www.geeksforgeeks.org/operating-systems/	M1-M6
2	https://www.tutori alspoint.com	https://www.tutorialspoint.com/operating_system/index.h tm	M1-M6



# Mini Project Hours Distribution:

Sr. No	Work to be done	No. of Hours	Cognitive levels of attainment as per Bloom's Taxonomy
1	Study Research papers and select a mini	4	L1.L2
2	Project Title and Modules Identification	4	L1,L2
3	Design & Methodology	2	L1,L2
4	Implementation of Module 1	4	L1,L2,L3
5	Result Phase I	4	L1,L2,L3
6	Implementation of Module 2	4	L1,L2
7	Result Phase II and Validate Modules	4	L1,L2,L3,L4, L5
8	Report Writing	4	L1,L2
	Total Hours	30	



# TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP)

(Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019

#### T.E. Semester –V

Ce

Estd. in 2001

B.E. (Computer Engineering)				T.E.	. SEM: V				
Course Name: Professional Elective 1(Mobile Computing)					Course Co	de: PEC-CS501	2		
Teaching Scheme (Program Specific) Examinati					ion Scheme (Form	ative/ Summat	ive)		
Modes of Teaching / Learning / Weightage Modes of Continuous Assessment / Evaluatio				tion					
	Ho	ours Per We	ek		The (1	eory 00)	Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	Т₩	
3	-	2@	5	4	25	75	25	25	150
		IA: In-	Semester A	Assessmer	nt - Pap	er Dura	tion – 1.5 Hours		
ESE: End Semester Examination - Paper Duration - 3 Hours									
The	The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance / Learning Attitude (20%)								
Prereau	isite: Basic	s of Progran	ming (Java	a). Compu	ter Netv	vorks			

**<u>Course Objective</u>**: This course introduces the basic concepts and principles in mobile computing. It covers the TCP/IP extensions for mobile networking and provides opportunities to the students to gain hands-on experiences in developing mobile applications.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

SN	Course Outcomes	RBT level
1	Identify with the basic concepts and principles in mobile computing.	L1, L2
2	Understand the components and functioning of mobile networking.	L1, L2, L3
3	Describe the technologies in telecommunication with their underlying architectures.	L1, L2
4	Explain mobility management	L1, L2, L3
5	Illustrate how mobile Ad-hoc networks function	L1, L2, L3
6	Implement small android based applications.	L1, L2, L3

Module	Topics	Hrs	<b>RBT Levels</b>
No.			
1	INTRODUCTION TO MOBILE COMPUTING		
	Mobile Computing vs. Wireless Networking ; Mobile Computing Applications; Characteristics of Mobile computing; Medium Access Control: Motivation for specialized MAC, Introduction to multiple Access techniques (MACA) , Wireless MAC Issues	06	L1,L2
2	MOBILE IP AND TCP		
	Mobile IP: IP Packet Delivery, Agent Advertisement and Discovery,		
	Registration, Tunneling and Encapsulation, Reverse Tunneling, Routing	0.0	
	(DSDV,DSR)	09	L1, L2,L3
	Mobile TCP: Traditional TCP, Classical TCP Improvements (like Indirect		

COMP ENGINEERS	TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	Estd. in 2001	©
	TCP, Snooping TCP & Mobile TCP, Fast Retransmit/ Fast Recovery, Transmission/Timeout Freezing, Selective Retransmission )		
3	MOBILE TELECOMMUNICATION SYSTEMS GSM Mobile services, System Architecture, Radio interface, Protocols, Localization and Calling, Handover, security (A3,A5 & A8); GPRS system and protocol architecture; UTRAN, UMTS core network ; Improvements on Core Network	07	L1, L2
4	MOBILITY MANAGEMENT Co- channel Interference; Mobility: Types of Handoffs; Location Management: HLR-VLR scheme, Hierarchical scheme, Predictive Location management schemes; Cellular IP; PSTN.	07	L1, L2, L3
5	MOBILE AD-HOC NETWORKS Ad-Hoc Networks: Basic Concept, Characteristics , Applications ;Design Issues; Routing :Essential of Traditional Routing Protocols, Popular Routing Protocols; Vehicular Ad Hoc networks ( VANET);MANET Vs VANET; Security in ad-hoc networks	08	L1, L2, L3
6	MOBILE APPLICATION DEVELOPMENT Structure of Mobile Computing Application; Characteristics of mobile devices; Native applications vs. Web-Applications; Internet Protocols for mobile apps; Mobile Platforms: Introduction to Android, Layers, android components, mapping application to process. Android development basics. Hardware tools, Software tools, Android SDK features.	08	L1, L2, L3
	Total hours	45	

S. No.	Title	Authors	Publisher	Edition	Year
1	Mobile Computing	Raj Kamal	Oxford University	Second	2011
			Press	Edition	
2	Mobile Communication	Jochen Schilller	Addision wisely,	Second	2004
			Pearson Education	Edition	
3	Fundamentals of Mobile	Pattnaik, Prasant Kumar	PHI Learning Pvt.	Second	2016
	Computing		Ltd.	Edition	
4	Mobility Protocols and	Ashutosh Dutta, Henning	IEEE Press, Wiley	First	2015
	Handover optimization:	Schulzrinne	Publication	Edition	
	Design, Evaluation and				
	Application				

# **Online References:**

S. No.	Website Name	URL	Modules Covered
1	cse.iitb.ac.in	https://www.cse.iitb.ac.in/~mythili/teaching/cs653_spring201 4/index.html	M1, M2, M3
2	www.tutorialspoint.com	https://www.tutorialspoint.com/umts/umts_cellular_concepts _mobility_management.htm	M4



4	nptel.ac.in	https://nptel.ac.in/courses/106105160/	M5
3	learn.saylor.org	https://learn.saylor.org/course/view.php?id=95&sectionid=97 8	M6

# Mini Project Hours Distribution

Sr. No	Work to be done	No. of Hours	Cognitive levels of attainment as per Bloom's Taxonomy
1	Study articles and research papers: Identification of mini-project title.	4	L1.L2,L3
2	Finalizing title and identifying different modules to be developed.	4	L1,L2,L3
3	Design and Methodology: Finalizing design approach and tools for implementation.	2	L1,L2,L3
4	Implementation of Modules Phase I	4	L1,L2,L3
5	Result Phase I	4	L1,L2,L3,L4
6	Implementation of Modules Phase II	4	L1,L2,L3
7	Result Phase II and Validate Modules	4	L1,L2,L3,L4, L5
8	Report Writing	4	L1,L2,L3
	Total Hours	30	



# <u>TCET</u> DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019 Ice

Estd. in 2001

#### T.E. Semester –V

<b>B.E.</b> (Computer Engineering)							T.E	. SEM: V	
Course Name: Professional Elective 1(Advance Database management system)							Course Co	de: PEC-CS501	.3
Teaching Scheme (Program Specific) Examination							ion Scheme (Formative/ Summative)		
Modes of Teaching / Learning / Weightage Modes of Continuous Assessment / Evaluation							tion		
Hours Per Week					The (1	eory 00)	Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2@	5	4	25	75	25	25	150
IA: In-Semester Assessment - Paper Duration – 1.5 Hours									
ESE: End Semester Examination - Paper Duration - 3 Hours									
The	The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance / Learning Attitude (20%)								
Prereau	isite: Basic	s of Databas	e						

Course Objective: The objective of the course is to study various Advanced Database concepts like Query Processing, Database Security and to study various Advanced Databases like Distributed Databases, Document Oriented Databases, Temporal, Spatial, Multimedia and Mobile Databases.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Apply appropriate security techniques database systems	L1, L2, L3
2	Apply Query Optimization and Measure Query cost	L1, L2, L3
3	Describe the concepts of Distributed Database Basics	L1, L2
4	Analyze Distributed database for better resource management.	L1, L2, L3, L4
5	Demonstrate the understanding of the concepts of Document Oriented Databases.	L1, L2, L3, L4
6	Discuss Advanced data models for real life applications	L1, L2

Module	Topics	Hrs.	Cognitive levels
No.			of attainment as
			per Bloom's
			Taxonomy
1	Data Security		L1, L2, L3
	Introduction to Database Security Issues; authorization, Discretionary		
	Access Control Based on Granting and Revoking Privileges, Mandatory	6	
	Access Control and Role-Based.		
	Access Control for Multilevel Security		
	• SQL Injection		
	• Introduction to Statistical Database Security, Introduction to Flow		
	Control		



2	Ouery processing and Optimization		L1, L2, L3
-	• Overview		21, 22, 20
	• Measures of Ouerv cost	8	
	• Selection operation	-	
	• Sorting		
	• Join Operations, and other Operations		
	Evaluation of Expression Ouerv Optimization :		
	• Translations of SOL Oueries into relational algebra		
	• Heuristic approach & cost based optimization		
3	Overview of Distributed Database System		L1, L2
	Features and Design Issues of Distributed Databases, Types of		
	Distributed Databases, Distributed Database Architectures.	6	
4	Distributed Database Design, Transaction, Concurrency and	10	L1. L2. L3. L4
	Recovery	-	, , ,
	Data Fragmentation, Replication, Allocation Techniques in Distributed		
	Databases, Transparencies for Distributed Database Design, Distributed		
	Transaction Management in Distributed Databases, Distributed		
	Concurrency Control (locking), Recovery in Distributed Databases		
	{2PC/3PC) and Deadlock management.		
5	Document Oriented Database	9	L1, L2, L3,L4
	Need of object oriented database, Impedance matching problem between		
	OO languages and Relational database, Case study db4O, Need of		
	Document Oriented database, difference between Document Oriented		
	Database and Traditional database. Types of encoding XML, JSON,		
	BSON, Representation XML, Json Objects. Case study on document		
	Oriented Database		
6	Advanced Data Models	6	L1, L2
	Temporal data models: - Aspects of valid time, Bi-temporal time and		
	bi-temporal time with examples of each.		
	Spatial model :- Types of spatial data models - Raster, Vector and Image		
	Mobile databases, Multimedia databases.		
	Total Hours	45	

	Title	Authors	Publisher	Edition	Year
1	Fundamentals of Database Systems	Elmasri & Navathe	PEARSON Education.	Seventh Edition	2016
2	Database systems concepts	Korth, Silberschatzsudarshan	McGraw Hill	Seventh Edition	2016
3	Database Management System	Raghu Ramkrishnan & Johannes Gehrke	Tata McGraw- Hill Edition.	Third Edition	
4	Learning MySQL and Mariadb	Ruosell J.T. Dyer	O'Reilly		



## **Online Resources:**

S.	Website Name	URL	Modules Covered
1	www.techotonia.co	https://www.techotopia.com/index.php/Mandatory_Discr	M1
1	m	etionary,_Role_and_Rule_Based_Access_Control	111
2	www.geeksforgeek s.org	https://www.geeksforgeeks.org/sql-query-processing/	M2
3	www.tutorialspoint .com	https://www.tutorialspoint.com/distributed_dbms/distribu ted_dbms_databases.htm	M3-M6

# **Mini Project Hours Distribution**

Sr. No	Work to be done	No. of Hours	Cognitive levels of attainment as per Bloom's Taxonomy
1	Identification and Study of Advanced Database	8	L1,L2
2	Project Title Identification	2	L1,L2
3	Graphical User Interface Design	2	L1,L2,L3
4	Database Design	2	L1,L2,L3
5	Linking of GUI with Advanced Database	8	L1,L2,L3
6	Testing of Mini Project	2	L1,L2, L3
7	Preparation of Report	6	L1,L2
	Total Hours	30	



## TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP)

(Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019

#### T.E. Semester –V

Ce

Estd. in 2001

B.E. (Computer Engineering)							T.E	. SEM: V		
Course Name: Professional Elective 1(Multimedia Systems)						Course Code: PEC-CS5014				
Teaching Scheme (Program Specific) Examin						aminati	ion Scheme (Formative/ Summative)			
Mod	es of Teach	ing / Learn	ing / Weig	htage	M	odes of	Continuous Assess	sment / Evalua	tion	
Hours Per Week					The (1	eory 00)	Practical/Oral (25)	Term Work (25)	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW		
3	-	2@	5	4	25	75	25	25	150	
	IA: In-Semester Assessment - Paper Duration – 1.5 Hours									
ESE: End Semester Examination - Paper Duration - 3 Hours										
The	The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely completion of practical (40%) and Attendance / Learning Attitude (20%)									
Prerequ	isite: Com	uter Fundar	nents and C	Graphics						

**<u>Course Objective:</u>** The course should be able to introduce students about basic fundamentals and key aspects of Multimedia system, provide knowledge of compression techniques of different multimedia components, students to understand multimedia communication standards along with technology environment & provide an opportunity to gain hands-on experience in building multimedia applications.

#### <u>Course Outcomes:</u> Upon completion of the course students will be able to

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	To identify basics of multimedia and multimedia system architecture.	L1, L2, L3
2	To understand different multimedia components	L1, L2
3	To explain file formats for different multimedia components.	L1, L2, L3, L4
4	To analyze the different compression algorithms.	L1, L2, L3, L4
5	To describe various multimedia communication techniques.	L1, L2, L3
6	To apply different security techniques in multimedia environment.	L1, L2, L3, L4

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Multimedia System: An Introduction		
	Multimedia Elements, Multimedia Applications, Multimedia System		
	Architecture, Evolving Technologies for Multimedia Systems, Defining	8	L1, L2, L3
	Objects for Multimedia Systems, Multimedia Data Interface Standards, The		
	need for Data Compression, Multimedia Database		
2	Compression & Decompression And Data File format Standards	10	1112
	Types of Compression, Binary Image Compression Schemes		L1, L2

	TCET DEPARTMENT OF COMPUTER ENGINEERING (COM (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019 Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	P) to ) Estd.	
	Color, Gray Scale and Still Video Image Compression, Video Image		
	Compression, Audio Compression, Rich Text Format, TIFF File Format,		
	Resource Interchange File Format (RIFF), MIDI File Format		
	JPEG DIB File Format for Still and Motion Image, AVI File Format, MPEG		
-	Standards	10	
3	Multimedia Input/output technologies& Storage Retrieval Technologies	10	
	Key Technologies Issues, Pen Input, Video and Image Display Systems, Print		
	output Technologies, Image Scanners, Digital Camera, Video Images and		L1, L2, L3,
	Animation, Full-Motion Video, Magnetic Media Technology		L4
	Optical Media, Hierarchical Storage Management, Cache Management For		
	Storage Systems		
4	Architectural & Telecommunications Considerations And Multimedia		
	Application Design	0	
	Specialized Computational Processors, Memory Systems	8	L1, L2, L3,
	Multimedia Board Solutions, LAN/WAN Connectivity, Distributed Objects		L4
	Models, Multimedia Applications Classes, Types of Multimedia System		
	Virtual Reality Design, Components of Multimedia Systems, Distributed		
	Application Design Issues		
3	Multimedia Authoring & User Interface And Hypermedia Messaging		
	Multimedia Authoring System, Hypermedia Application Design	5	11 10 10
	Considerations, User Interface Design, Mobile Messaging, Hypermedia,	5	L1, L2, L3
	Message Components, Hypermedia Linking and Embedding, Creating		
(	Hypermedia Messages		
6	Distributed Multimedia Systems		
	Components of a Distributed Multimedia System, distributed Client-Server	4	L1, L2, L3, L4
	Multi armen Network Tenela vice Distributed Multimedia D (1)	4	L4
	, wum-server Network Topologies, Distributed Multimedia Databases	45	
	I otal Hours	45	
		i	

SN	Title	Authors	Publisher	Edition	Year
1	Multimedia Systems Design	Prabhat K Angleigh& Kiran Thakrar	PHI	1st	2005
2	Multimedia Communication Systems: Techniques, Standards & Networks	K. R. Rao, Zoran S. Bojkovic&Dragorad A. Milovanovic	ТМН	1th	2010
3	Multimedia Systems	K. Buford	PHI	3rd	2012
4	Fundamentals of Multimedia	Ze-Nian Li & Mark S. Drew	PHI	2nd	2011
5	Multimedia Computing Communications & Applications,	Ralf Steinmetz & Klara Nahrstedt,	Pearson	1 st	2012

# **Online Resources:**

S. No.	Website Name	URL	Modules Covered
1	www.springer.com	https://www.springer.com/gp/book/9783540408673	M1-M6



l	2	https://books.googl	https://books.google.co.in/books?id=34Uuim67mvUC&prints	
l		e.co.in/	ec=frontcover&source=gbs_ge_summary_r&cad=0#v=onepag	M1-M6
l			e&q&f=false	

# **Mini Project Hours Distribution**

Sr. No	Work to be done	No. of Hours	Cognitive levels of attainment as per Bloom's Taxonomy
1	Study tool for implementation	2	L1,L2
2	Project Title and Course Identification	2	L1,L2
3	Choose File Format and Compression techniques	4	L1,L2
4	Perform task related to compression or Authoring tool	2	L1,L2,L3
5	Select Authoring tool	4	L1,L2,L3
6	Design the project using Authoring Tool	2	L1,L2
7	Design and implement the Authoring System.	4	L1,L2,L3,L4
8	Design a project in Authoring system	2	L1,L2,L3,L4
9	Test and Evaluate Model designed in Authoring tool.	4	L1,L2,L3,L4,L5
10	Prepare report	4	L1,L2
	Total Hours	30	



# **TCET**

DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3<sup>rd</sup> Cycle Accreditation w.e.f. 1<sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCE Text Computer Scheme - 2019

#### T.E. Semester –V

Ice

Estd. in 2001

B.E. (Computer Engineering)					T.E	. SEM: V			
Course Name: Professional Elective 1(Machine Learning)					Course Co	de: PEC-CS501	5		
Teaching Scheme (Program Specific) Examinati					ion Scheme (Form	ative/ Summat	ive)		
Mod	es of Teach	ing / Learn	ing / Weig	htage	Μ	odes of	Continuous Assess	sment / Evalua	tion
Hours Per Week			Theory (100)Practical/Oral (25)Term Work (25)To			Total			
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
3	-	2@	5	4	25	75	25	25	150
		IA: In-	Semester .	Assessmen	nt - Pap	er Dura	tion – 1.5 Hours	-	
	ESE: End Semester Examination - Paper Duration - 3 Hours								
The	<b>The weightage of marks for continuous evaluation of Term work/Report:</b> Formative (40%), Timely completion of practical (40%) and Attendance / Learning Attitude (20%)								
Prerequ	isite: Linea	r Algebra, C	Calculus, Pr	obability,	Statistic	s			

**<u>Course Objective:</u>** The course should be able to introduce Machine Learning techniques and become familiar with its types.

#### Course Outcomes: Upon completion of the course students will be able to

SN	Course Outcomes	Cognitive levels of attainment as per Bloom's
		Taxonomy
1	Understand basics of ML	L1, L2
2	Apply preprocessing techniques	L1, L2, L3
3	Apply regression for learning and assess the outcome	L1, L2, L3, L4
4	Apply classification for learning and assess the outcome	L1, L2, L3, L4
5	Apply optimization techniques for performance enhancement	L1, L2, L3, L4
6	Apply unsupervised and reinforcement learning concepts and assess the outcome	L1, L2, L3, L4

Module No.	Topics	Hrs.	Cognitive levels of attainment as per Bloom's Taxonomy
1	Introduction to Machine Learning		
	Machine Learning terminology, Types of Machine Learning, Issues in		L1 L2
	Machine Learning, Application of Machine Learning, Steps in developing	5	11, 12
	ML application, How to choose the right algorithm		
2	Data Preprocessing		
	Data Transformation, Data Handling (Missing, Imbalanced), Outlier detection	10	L1, L2, L3
	and Visualization, Feature selection and extraction		
3	Supervised Learning with Regression		111212
	Simple Linear, Gradient Descent, Multiple Linear, Polynomial,	5	$L_1, L_2, L_3, L_4$
	Regularization, Evaluation Metric, Use case		L4

<b>TCET</b> <b>DEPARTMENT OF COMPUTER ENGINEERING (COMP)</b> (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019) Choice Based Credit Grading System with Holistic Student Development (CBCSS - H 2019) Under ICET Autonomy Scheme 2018	
under TCET Autonomy Scheme - 2019	

4	Supervised Learning with Classification		
	k Nearest Neighbor, Logistic Regression, Naïve Bayes, Linear SVM,	13	111212
	Kernels, Decision Tree (CART), Issues in DT learning, Ensembles (Bagging		$L_{1}, L_{2}, L_{3}, L_{4}$
	- Random Forest, Boosting - AdaBoost), Evaluation Metric, Use case		L4
5	Optimization Techniques		L1, L2, L3,
	Model Selection techniques, Cross Validation, Grid Search method	6	L4
6	Unsupervised Learning with clustering and Reinforcement Learning		
	Expectation Maximization algorithm, Use case		L1, L2, L3,
	Elements of Reinforcement Learning, Online Learning (Temporal	6	L4
	Difference), Use case		
	Total Hours	45	

SN	Title	Authors	Publisher	Edition	Year
1	Machine Learning In Action	Peter Harrington	DreamTech Press	1 <sup>st</sup>	2012
2	Introduction to Machine Learning	Ethem Alpaydın	MIT Press	4 <sup>th</sup>	2020
3	Machine Learning	Tom M. Mitchell	McGraw Hill	Indian	1997
4	Machine Learning An Algorithmic Perspective	Stephen Marsland	CRC Press	2 <sup>nd</sup>	2011
5	Machine Learning — A Probabilistic Perspective	Kevin P. Murphy	MIT Press	1 <sup>st</sup>	2012
6	Pattern Recognition and Machine Learning	Christopher M. Bishop	Springer		2006
7	Elements of Statistical Learning	Trevor Hastie, Robert Tibshirani, Jerome Friedman	Springer	2 <sup>nd</sup>	2017

# **Online Resources:**

S.	Website Name	URL	<b>Modules</b> Covered
No.			
1	www.analyticvidh	https://www.analyticsvidhya.com/%20machine%20learning/	M1-M6
	ya.com		
2	www.towardsdatas	https://towardsdatascience.com/machine-learning/home	M1-M6
	cience.com		
3	www.coursera.org	https://www.coursera.org/learn/machine-	
		learning?utm_source=gg&utm_medium=sem&utm_content=0	
		7-StanfordML-	
		IN&campaignid=1950458127&adgroupid=69480953983&de	
		vice=c&keyword=machine%20learning%20online%20course	M1-M6
		&matchtype=b&network=g&devicemodel=&adpostion=1t2&	
		creativeid=351281535285&hide_mobile_promo&gclid=Cj0K	
		CQiAn8nuBRCzARIsAJcdIfMYXtdIwVvfyr6ee ewWcWrBd	
		FmGWrJnWif67PHGt-sEH6r68QbhUoaAvmJEALw_wcB	



# **Mini Project Hours Distribution**

Sr. No	Work to be done	No. of Hours	Cognitive levels of attainment as per Bloom's Taxonomy
1	Study tool for implementation	2	L1,L2
2	Project Title and Course Identification	2	L1,L2
3	Choose Data	2	L1,L2
4	Perform EDA	2	L1,L2,L3
5	Perform Feature Engineering	2	L1,L2,L3
6	Chose Model	2	L1,L2
7	Train and Validate Model	2	L1,L2,L3,L4
8	Tune Hyper parameters	2	L1,L2,L3,L4
9	Test and Evaluate Model	2	L1,L2,L3,L4,L5
10	Prepare report	2	L1,L2
	Total Hours	30	



	B.E. (Computer Engineering)					T.E.	SEM: V		
	Course Name Indian constitution					Course C	ode: MC-CS501		
Te	aching Sch	eme (Prog	ram Speci	fic)	Exam	ination	Scheme (Format	ive/ Summativ	/e)
Mode	s of Teach	ing / Learn	ing / Weig	ghtage	Mode	es of Co	ntinuous Assessm	ent / Evaluati	on
Hours Per Week					Theor (100)	.у )	Practical/Oral (25)	Term Work (25)	Total
Theory	Tutorial	Practical	Contact Hours	Credits	IA	ESE	PR/OR	TW	
1	-	-	1	(Non- Credit)	Passing is mandatory for this course	-	-	25	25
Prerequ	isite: -								

**<u>Course Objectives:</u>** To understand fundamental of Indian constitutional system, Union structure, Judiciary Structure with hierarchy and it function

#### <u>Course Outcomes:</u> Upon completion of the course students will be able to:

SN	Course Objectives	Cognitive levels of attainment as per Bloom's Taxonomy
1	Understand the rights and duties of Individual and government	L1, L2
2	To understand the government structure and hierarchy	L1, L2
3	To understand right of Indian who residing in India or outside India and understand citizenship law	L1, L2
4	To understand and apply the personal, social and economic rights to citizens of India	L1, L2, L3
5	To analyze the functions and powers of state and its limbs i.e. Legislature, Executive.	L1, L2, L3, L4
6	Understand the structure and modalities of state i.e. legislature, executive and judiciary	L1, L2, L3, L4

Modul e No.	Topics	Hrs ·	Cognitive levels of attainment as per Bloom's Taxonomy
1.0	Introduction Constitution' meaning of the term,, Indian Constitution: Sources and constitutional history, Features: Citizenship, Preamble, Fundamental Rights and Duties, Directive Principles of State Policy	2	L1, L2
2.0	Union Government and its Administration	2	

	TCET DEPARTMENT OF COMPUTER ENGINEERING (COMP) (Accredited by NBA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> July 2019) (Choice Based Credit Grading System with Holistic Student Development (CBCGS - H 2019) Under TCET Autonomy Scheme - 2019	Estd. in	(B) (C) (C) (C) (C) (C) (C) (C) (C) (C) (C
	Structure of the Indian Union: Federalism, Centre- State relationship, President: Role, power and position, PM and Council of ministers, Cabinet and Central Secretariat, Lok Sabha, Rajya Sabha		L1, L2
3.0	Citizenship Citizenship at the commencement of the Constitution ,Rights of citizenship of certain persons who have migrated to India from Pakistan, Rights of citizenship of certain migrants to Pakistan , Rights of citizenship of certain persons of Indian origin residing outside India , Persons voluntarily acquiring citizenship of a foreign State not to be citizens, Continuance of the rights of citizenship, Parliament to regulate the right of citizenship by law .	3	L1, L2
4.0	Fundamental Rights           Definition , Laws inconsistent with or in derogation of the fundamental rights,           Right to equality, Right to freedom, Right against exploitation, Right to freedom of religion, Cultural and educational Right, Right to constitution Remedies.	2	L1, L2, L3
5.0	The Union           Executive( President & Vice President)           General (office of Parliament)           Conduct of Business	3	L1, L2, L3, L4
6.0	The Union Judiciary         Establishment and constitution of Supreme Court Salaries	3	L1, L2, L3, L4
	Total	15	

S. No.	Title	Authors	Publisher	Edition	Year
1	The Constitution of	Bare Act	Government of	NA	2020
	India		India		
2	Introduction to the	D.D. Basu	Lexis Nexis	24th	2019
	Constitution of India			Edition	
3	Indian Constitutional	M.P Jain	Lexis Nexis	8th Edition	2018
	Law				



#### T.E. Semester –V

	B.E.( Computer Engineering)				SI	EM: VI		
	Course Name: Summer Internship					Course Code: SI-CS501		
Те	eaching Sch	neme (Prog	ram Speci	fic)	Examinatio	on Scheme (Form	ative/ Summat	ive)
Modes of Teaching / Learning / Weightage				htage	Modes of C	Continuous Assess	ment / Evalua	tion
Total Hours : Maximum 2 Weeks ( 60 to 80 Hours during summer vacation)			o 80			TW	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	-	-	50	
-	-	30	30	1			50	50
Note : 1.	Note : 1. Internship will be done in institute laboratory in collaboration with industries.							

2. Evaluation and assessment will be done as per AICTE guidelines.

Prerequisite: Fundamental knowledge of respective programmes

## **Course Objectives:**

To get industry like exposure in the institute laboratories by carrying out activities / projects. Also design innovative techniques / methods to develop the products.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
1	Apply subjects knowledge in the college laboratories for carrying out projects	L3, L4,L5
2	Developed innovative techniques / methods to develop the products	L3, L4,L5
3	Contribute for the society	L3, L4,L5

Module No.	Topics	Cognitive levels of attainment as per Bloom's Taxonomy
	Program Specific Internship	· · · · · ·
1	<ul> <li>Training and certification on emerging technologies in domains offered by Department of Computer Engineering</li> <li>Applying classroom and laboratory knowledge to design, develop and deploy the products</li> </ul>	L3, L4,L5
	Inter disciplinary Internship	
2	<ul> <li>To explore and understand issues and challenges in the other disciplines (EXTC, ELEX, MECH and CIVIL)</li> <li>Design , develop and deploy cost effective products using</li> </ul>	L3, L4,L5
	multidisciplinary approach	



	Industry Specific Internship	
3	• To explore and understand issues and challenges in industry	
	• Developing solutions for industry specific problems	L3, L4,L5
	• Design, develop and deploy products for startup and SMEs	
	Interpersonal Internship	
	• To develop interpersonal skills such as leadership, marketing	
4	,publicity and corporate ethics and communication	L3, L4,L5
	• To get competence in problem solving, presentation, negotiation	
	skills	
	Social Internship	
5	• Identify and study different real life issues in the society	12 1415
	• Identify societal problems and provide engineering solutions to	L3, L4,L3
	solve these problems	
	Academic Internship	
	• Study report preparation, preparation of presentations, copy table	
	book preparation, business proposal and IPR	
	• Capture aspirations & expectations through interviews of students.	
	• Ways to connect research in technical institutes with industry.	L3, L4,L5
6	• Taking inputs from self, local stakeholders and global stake holders	
Ū	which will help to develop process with comparative and	
	competitive study.	

Sr.	Title	Authors	Publisher	Edition	Year
No.					
1	The Ultimate Guide to				
	Internships: 100 Steps to Get a	Fric Woodard	Allworth	I	2015
	Great Internship and Thrive in It		Anworth	1	2015
	(Ultimate Guides)				

#### **Online References:**

Sr.	Website Name	URL	Modules
No.			Covered
1	https://www.letsintern.c	https://www.letsintern.com/internships/summer-internships	M1-M6
	om/		
1	https://codegnan.com	https://codegnan.com/blog/benefits-of-internships-and-	M1-M6
		importance	
2	https://www.honorsociet	https://www.honorsociety.org/articles?category=internships	M1-M6
	y.org		



#### T.E. Semester –V

	B.E.	(Compute	r Enginee	ring)			T.E. SEM:	V		
Cour	Course Name Professional Skill V (Web Develo				Course Name Professional Skill V (Web Develop		oment)		Course Code: HSP	D-CSPS501
Teaching Scheme (Program Specific)				Exam	ination	Scheme (Formative	e/ Summative)			
Modes of Teaching / Learning / Weightage				Modes of Continuous Assessment / Evaluation			t / Evaluation			
Hours Per Week					Presentatio	on	Report	Total		
Theory	Tutorial	Practical	Contact Hours	Credits	AC		AC			
1	-	2	3	2	50		25	- 75		
The	AC- Activity evaluation The weightage of marks for continuous evaluation of Term work/Report: Formative (40%), Timely									
Prerequ	iisite: Com	puter Basic	of practic	al (40%) a	ind Attendar	nce/Lear	ning Attitude (20%)			

**<u>Course Objectives:</u>** By the end of the course students will be able to design and implement static and dynamic websites.

#### **<u>Course Outcomes:</u>** Upon completion of the course students will be able to

S.N.	Course Outcomes	Cognitive level attainment as per revised Bloom's Taxonomy
1	Understand different components in web technology and to know about web servers.	L1, L2
2	Develop an interactive Web pages using HTML/XHTML.	L1, L2, L3, L4
3	Present a professional document using Cascaded Style Sheets.	L1, L2, L3, L4
4	Construct websites for user interactions using JavaScript and JQuery.	L1, L2, L3, L4, L5
5	Know the different information interchange formats like XML and JSON.	L1, L2, L3, L4
6	Develop Web applications using PHP.	L1, L2, L3, L4, L5

Module No.	Topics	Cognitive level attainment as per revised Bloom's Taxo nomy
1	Introduction to the Internet	L1,
	The World Wide Web, Web Browsers, Uniform Resource Locators, WWW	L2
	Architecture – SMTP – POP3 – File Transfer Protocol	
	The Hypertext Transfer Protocol, HTTP request – response — Generation of dynamic	
	web pages- W3C Validator, How web works - Setting up the environment	
	(LAMP/XAMP/WAMP server)	
2	HTML/XHTML	L1, L2, L3,
	Basics of HTML, formatting and fonts, commenting code, color, hyperlink, lists, tables	L4
	Images, forms, XHTML, Meta tags, Character entities, frames and frame sets, Browser architecture and Web site structure. Overview and features of HTML5, Syntactic Differences between HTML and XHTML	



3	Introduction to Cascading Style Sheets	L1, L2, L3, L4
	Cascading Style Sheets: Levels of Style Sheets - Style Specification Formats, Selector Forms, Property-Value Forms, Font Properties, List Properties, Alignment of Text, Color, The Box Model, Background Images, The span and div Tags.	
4	Introduction to JavaScript	L1, L2, L3, L4,
	Introduction: client-side scripting-First program: Displaying a Line of Text-JavaScript Alert-Dynamic Page-Web Application-Variables in JavaScript-Data Types in JavaScript-Operators and Expressions-Simple If Statement- If Else Statement- Nested If Else Statement-Switch Case-For Loop-While Loop-Functions-Events-Arrays- Objects –Math and Date in JavaScript-Redirect to Another HTML Page	L5
5	Introduction to Data Interchange Formats	L1, L2, L3,
	XML: The Syntax of XML, XML Document Structure, Namespaces, XML Schemas, Displaying Raw XML Documents, Displaying XML Documents with CSS, XSLT Style Sheets, XML Applications. JSON(Basics Only): Overview, Syntax, Datatypes, Objects, Schema, Comparison with XML	L4
6	Introduction to PHP and MySQL	L1, L2, L3, L4,
	Basic commands with PHP examples, Connection to server, creating database, selecting a database, listing database, listing table names, creating a table, inserting data, altering tables, queries, deleting database, deleting data and tables, PHP myadmin and database bugs	LS

SN	Title	Authors	Publisher	Edition	Year
1	Internet &World Wide Web How to Program	P. J. Deitel, H.M. Deitel	Pearson education	4th Edition,	2010
2	Programming the World Wide Web	Robert W Sebesta	Pearson education	7th Edition,	2014
3	HTML 5	DT Editorial services	Dreamtech Press	2 <sup>nd</sup> Edition	2016
4	Web Technologies Black Book	Kogent Learning Solutions	Dreamtech Press	2 <sup>nd</sup> Edition	2016

## **Online References:**

S. No.	Website Name	URL	Modules
			Covered
1	W3schools	https://www.w3schools.com	M1-M6
2	Tutorialspoint	https://www.tutorialspoint.com	M1-M6
3	Javatpoint	https://www.javatpoint.com	M1-M6

#### List of Practical/ Experiments:

Practical	<b>Type of Experiment</b>	Practical/ Experiment Topic	Hrs.	Cognitive levels of
Number				attainment as per
				<b>Bloom's Taxonomy</b>
1		Explain Installation steps to LAMP / WAMP	2	L1
		/ XAMP.		
2	<b>Basic Experiments</b>	Sketch Simple web page using HTML5	2	L1, L2
3		Develop web page using CSS3 and HTML5.	2	L1, L2, L3

	DEPARTMEN (Accredited by NE Choice Based Cred	TCET TOF COMPUTER ENGINEERING ( BA for 3 years, 3 <sup>rd</sup> Cycle Accreditation w.e.f. 1 <sup>st</sup> Ju it Grading System with Holistic Student Development (CBCGS - H Under TCET Autonomy Scheme - 2019	<b>COMF</b> ily 2019] <sup>2019]</sup>	P) CCCC Etd. in 2001
4		Develop a Javascript web page illustrating	2	L1, L2, L3
5	Design Experiments	Tunctions and events Develop simple web page using PHP functions.	2	L1, L2, L3
6		Develop XML web page using DTD, XSL.	2	
7	Advanced Experiments	Develop a login page using PHP.	4	L1, L2, L3
8		Develop interactive web pages using PHP with database connectivity MYSQL.	4	L1, L2, L3
9	Mini/Minor Projects/ Seminar/	1. Online Second-hand Book Buying & Selling Portal	6	L1, L2, L3
		2. College E Print Service Management 3. Online Pizza Ordering System		
10	Case Studies/ Group Presentation	<ol> <li>Study on MYSQL database</li> <li>Study on different built-in methods of JavaScript</li> <li>Comparative study on Angular JS and Node</li> </ol>	4	L1, L2, L3
	Total Hours	μο	30	



				1.E. St	emester – v			
	В	.E. (Compu	ter Engine	ering)			T.E. SEM: V	
	Course Name Project Based Learning - I					Course Code: HSD-CSPBL		
Teaching Scheme (Program Specific)				Examination Scheme (Formative/ Summative)				
Modes of Teaching / Learning / Weightage				Modes of Continuous Assessment / Evaluation				
Hours Per Week				Presentation		Report	Total	
Theory	Tutorial	Practical	Contact Hours	Credits	AC		AC	
-	-	2	2	1	25		-	25
				AC- Activ	vity evaluation			
The	weightage	of marks fo	r continuo	us evaluat	tion of Term wo	ork/Rep	ort: Formative (40%), Tim	nely
		completion -	of practical	<u>(40%)</u> and	d Attendance/Le	arning A	ttitude (20%)	
Prerequi	isite: Comp	uter Fundan	nentals and	Knowledg	e of Programmi	ng Lang	uages	

**<u>Course Objective</u>**: The Course intends to aid students identify real world problems and apply computing fundamental and technical skill to find solutions to them.

#### **<u>Course Outcomes:</u>**Upon completion of the course students will be able to:

Sr. No.	Course Outcomes	Cognitive levels of attainment as per Bloom's
		Taxonomy
1	Interpret the basic real time problems.	L1, L2
2	Apply appropriate technologies and programming constructs to solve problems.	L1, L2, L3
3	Inspect the results obtained for documentation and presentation.	L1, L2, L3, L4

## **Projects Listing:**

Sr.	Title of Project	Type of Project
No.		
1	Implementing Online School Administration System	Application
2	Implementing Employee Transport Management System	Application
3	Implementing Online Course and Examination System	Application
4	Implementation of Online Secondhand Book Buying and Selling Portal	Application
5	Implementing Online Logistics Chatbot System	Application
6	Implementation of Online Newspaper Delivery Management System	Application
7	Design Online Health Shopping Portal with Product Recommendation	Core
8	Design Web-based Chat Application with webcam using PHP	Core
9	Design Internet based Discussion Forum	Core
10	Develop Customer targeted E-Commerce	Core



#### T.E. Semester –V

	В	.E. (Compu	ter Engine	ering)		T.E. SEM: V		
	Course Name Research Based Learning					Course Code: HSD-CSRBL5		
Teaching Scheme (Program Specific)				ïc)	Examination Scheme (Formative/ Summative)			
Modes of Teaching / Learning / Weightage				htage	Modes of Continuous Assessment / Evaluation			
Hours Per Week					Presentati	Presentation Report		
Theory	Tutorial	Practical	Contact Hours	Credits	AC	AC		
-	-	2	2	1	25	25	50	
				AC- Activ	vity evaluation			
Prerequ	isite: Mathe	ematical For	indation, Co	omputing N	Methods			

**<u>Course Objectives:</u>** This course is focused to engage the learner in research by upgrading domain knowledge by participation in technical quiz and debate, critical thinking, innovative idea generation and technical writing.

Course Outcomes:	Upon	completion	of the cou	urse students	will be able	e to:
------------------	------	------------	------------	---------------	--------------	-------

S.N.	Course Outcome	Cognitive level attainment as per revised Bloom Taxonomy
1	Upgrade the knowledge of latest technologies in their discipline in a competitive environment.	L1, L2
2	Create new idea for problem solving related to industry or societal issues.	L1, L2, L3
3	Understand research methodologies.	L1, L2, L3, L4
4	Students will be able to write a technical paper.	L1, L2, L3, L4, L5

Module No.		Topics	Hrs.	Cognitive level attainment as per revised Bloom Taxonomy
1	Technical	Quiz and Technical Debate	8	L1, L2
	I.	Quiz competition on technical topics from different domains with		
		50 MCQ (Questions will vary according to department).		
	II.	Formation of 8 teams for four topics. 2 teams (For and Against) for		
		topic I will debate first and the other teams will be audience.		
2	Idea gener	ation with design thinking aspects and related literature survey	7	L1, L2, L3
	I.	Introduction to design thinking and its stages.		
	II.	Formation of groups, generation of an idea and conducting literature		
		survey.		
3	Proof of co	oncept and validation of idea through survey	8	L1, L2, L3,L4
	Seminar on Research methodology			
	I.	Validate the idea by conducting the survey (through Google docs,		
		interviews or any other suitable method).		



		Total Hours	30	
	II.	Write a research paper on idea generated.		
	I.	Seminar or workshop on paper writing skills.		
	Documen	tation of Selected Idea and its validation		L3,L4,L5
4	Paper w	riting skills (Seminar/workshop)	7	L1, L2,
		8designing and conducting scientific research.		
	11.	Seminar on different research methods and procedures for		

# **References:**

Sr. No.	Title	Authors	Publisher	Edition	Year
1.	Writing Research Papers: A Complete Guide	James D. Lester	Longman	10th	2001
2.	Creativity in Product Innovation	Jacob Goldenberg	Cambridge University Press	Kindle	2002

## **Online References:**

Sr. No.	Website Name	URL	Modules Covered
1.	https://www.geeksforg eeks.org	https://www.geeksforgeeks.org/tag/c-quiz-references/	M1
2.	Interaction Design Foundation: Design Thinking	https://www.interaction-design.org/literature/topics/design- thinking	M2
3.	Scribbr: How to write a research methodology.	https://www.scribbr.com/dissertation/methodology/	M3
4.	https://www.statpac.co m	https://www.statpac.com/online-software-manual/Basic-Research- Concepts.htm	M4
5.	https://www.slideshare. net	https://www.slideshare.net/AsirJohnSamuel/lintroduction-to- research-methodology?next_slideshow=1	M4