

## GATE QUESTIONS

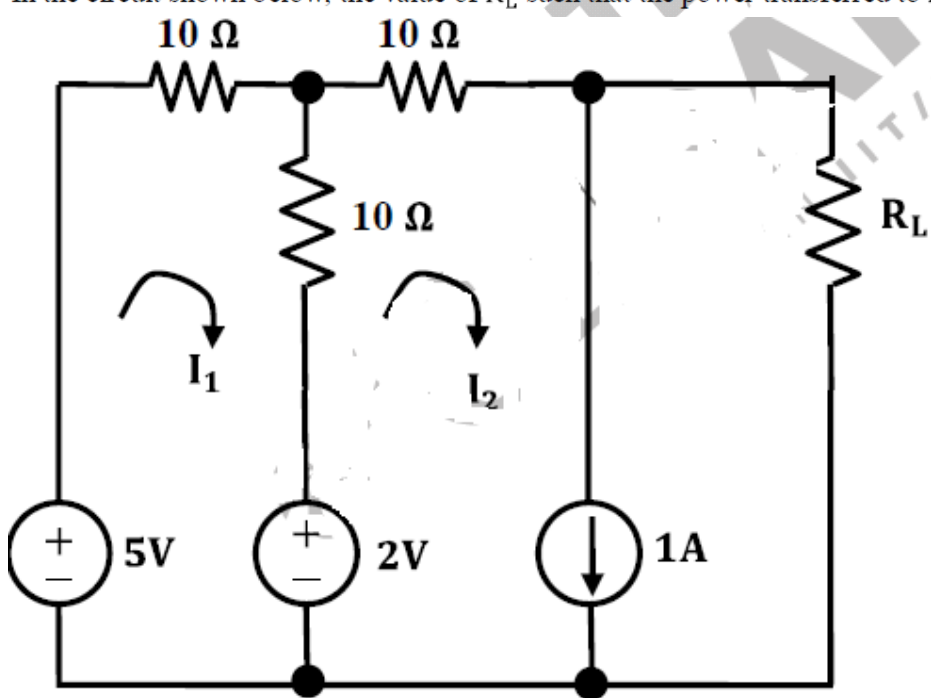
**A.Y.: 2021-2022 (Odd SEM)**

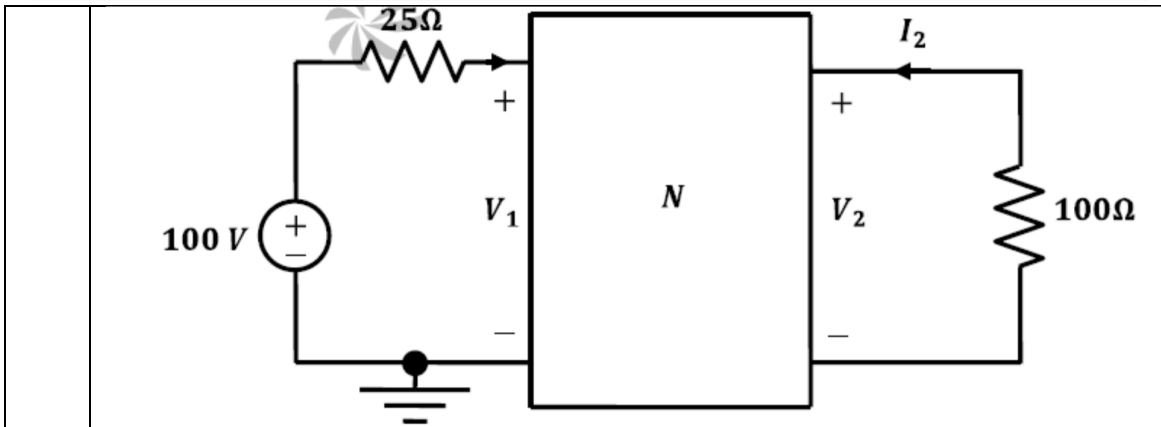
**Scheme: CBCGS-HME 2020**

Class: S.E. E&TC

Subject: Network Design

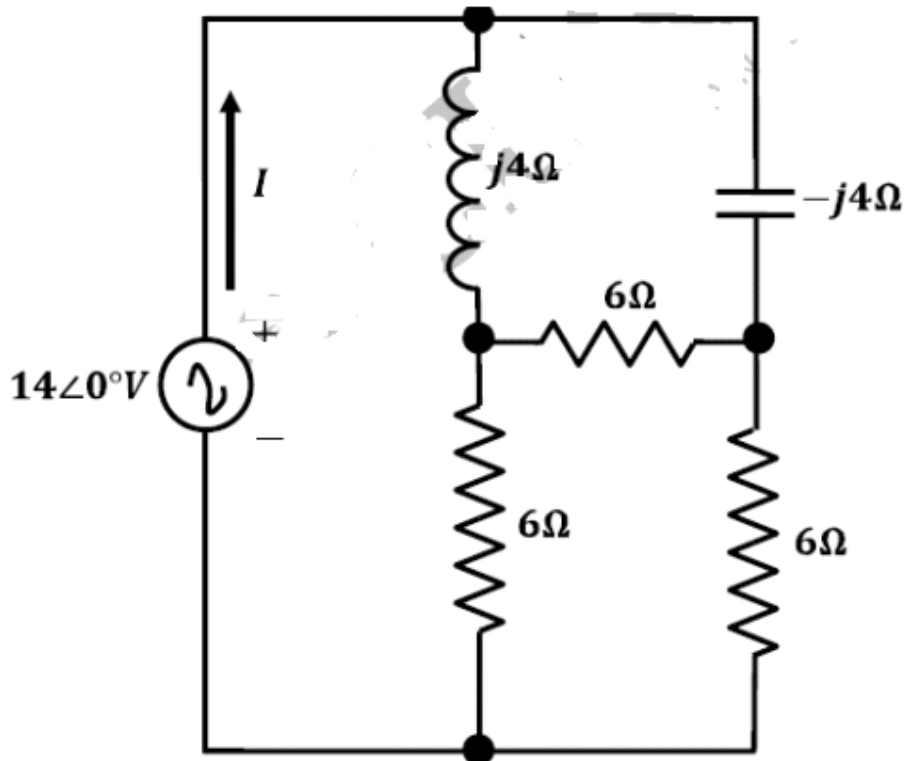
### 1 mark sample questions

1)	<p>In the circuit shown below, the value of <math>R_L</math> such that the power transferred to <math>R_L</math> is maximum</p> 
2)	<p>In the circuit shown below, the network N is described by the following Y matrix:  <math display="block">Y = \begin{bmatrix} 0.1 S &amp; -0.01 S \\ 0.01 S &amp; 0.1 S \end{bmatrix}</math>                     The voltage gain <math>\frac{V_2}{V_1}</math> is</p>



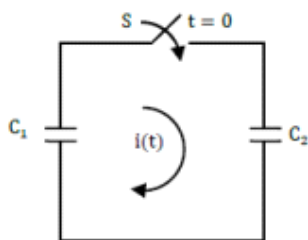
3)

In the circuit shown below, the current  $I$  is equal to

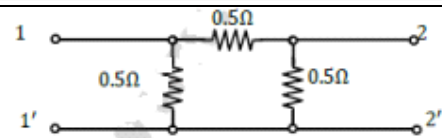
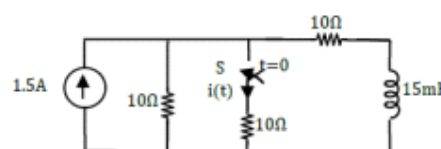
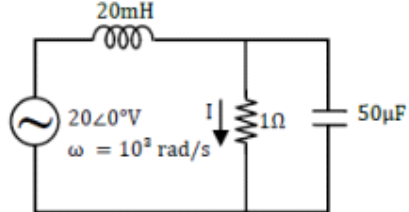
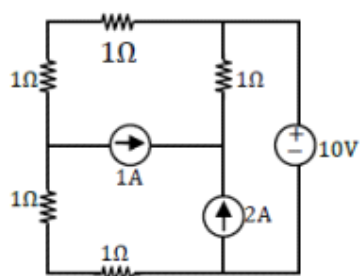


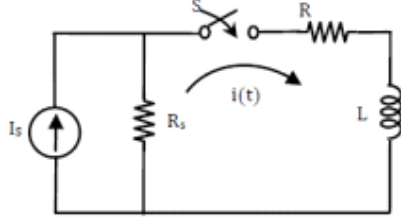
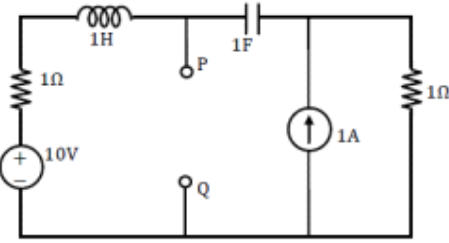
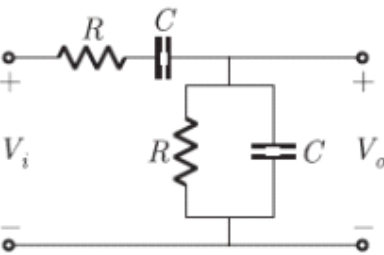
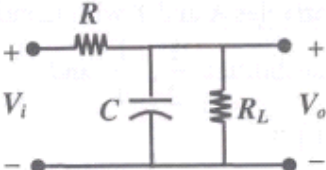
4)

In the following figure,  $C_1$  and  $C_2$  are ideal capacitors.  $C_1$  has been charged to 12 V before the ideal switch  $S$  is closed at  $t = 0$ . The current  $i(t)$  for all  $t$  is.



	<p>(A) Zero                      (B) a step function</p> <p>(C) an exponentially decaying function                      (D) an impulse function</p>
5)	<p>The impedance looking into nodes 1 and 2 in the given circuit is</p> <p>(A) 50 Ω                      (B) 100 Ω                      (C) 5 kΩ                      (D) 10.1 kΩ</p>
6)	<p>The transfer function <math>\frac{V_2(s)}{V_1(s)}</math> of the circuit shown below is</p> <p>(A) <math>\frac{0.5s + 1}{s + 1}</math>                      (B) <math>\frac{3s + 6}{s + 2}</math>                      (C) <math>\frac{s + 2}{s + 1}</math>                      (D) <math>\frac{s + 1}{s + 2}</math></p>
7)	<p>In the circuit shown below, if the source voltage <math>V_s = 100\angle 53.13^\circ</math> V then the Thevenin's equivalent voltage in volts as seen by the load resistance <math>R_L</math> is</p> <p>(A) <math>100\angle 90^\circ</math>                      (B) <math>800\angle 0^\circ</math>                      (C) <math>800\angle 90^\circ</math>                      (D) <math>100\angle 60^\circ</math></p>
8)	<p>For the two-port network shown below, the short-circuit admittance parameter matrix is</p>

	 <p>(A) <math>\begin{bmatrix} 4 &amp; -2 \\ -2 &amp; 4 \end{bmatrix} S</math>                      (C) <math>\begin{bmatrix} 1 &amp; 0.5 \\ 0.5 &amp; 1 \end{bmatrix} S</math></p> <p>(B) <math>\begin{bmatrix} 1 &amp; -0.5 \\ -0.5 &amp; 1 \end{bmatrix} S</math>                      (D) <math>\begin{bmatrix} 4 &amp; 2 \\ 2 &amp; 4 \end{bmatrix} S</math></p>
9)	<p>In the circuit shown, the switch S is open for a long time and is closed at <math>t = 0</math>. The current <math>i(t)</math> for <math>t \geq 0^+</math> is</p>  <p>(A) <math>i(t) = 0.5 - 0.125e^{-1000t} A</math>                      (C) <math>i(t) = 0.5 - 0.5e^{-1000t} A</math></p> <p>(B) <math>i(t) = 1.5 - 0.125e^{-1000t} A</math>                      (D) <math>i(t) = 0.375e^{-1000t} A</math></p>
10)	<p>The current I in the circuit shown is</p>  <p>(A) <math>-j1 A</math>                      (B) <math>j1 A</math>                      (C) <math>0 A</math>                      (D) <math>20 A</math></p>
11)	<p>In the circuit shown, the power supplied by the voltage source is</p>  <p>(A) <math>0 W</math>                      (C) <math>10 W</math></p> <p>(B) <math>5 W</math>                      (D) <math>100 W</math></p>
12)	<p>In the following circuit, the switch S is closed at <math>t=0</math>. The rate of change of current <math>\frac{di}{dt}(0^+)</math> is given by</p>

	 <p>(A) 0</p> <p>(B) <math>\frac{R_s I_s}{L}</math></p> <p>(C) <math>\frac{(R + R_s) I_s}{L}</math></p> <p>(D) <math>\infty</math></p>
13)	<p>The Thevenin's equivalent impedance <math>Z_{Th}</math> between the nodes P and Q in the following circuit is</p>  <p>(A) 1</p> <p>(B) <math>1 + s + \frac{1}{s}</math></p> <p>(C) <math>2 + s + \frac{1}{s}</math></p> <p>(D) <math>\frac{s^2 + s + 1}{s^2 + 2s + 1}</math></p>
14)	<p>The RC circuit shown in the figure is</p>  <p>(A) a low-pass filter</p> <p>(B) a high-pass filter</p> <p>(C) a band-pass filter</p> <p>(D) a band-reject filter</p>
15)	<p>If the transfer function of the following network is <math>\frac{V_o(s)}{V_i(s)} = \frac{1}{2 + sCR}</math>,</p> 

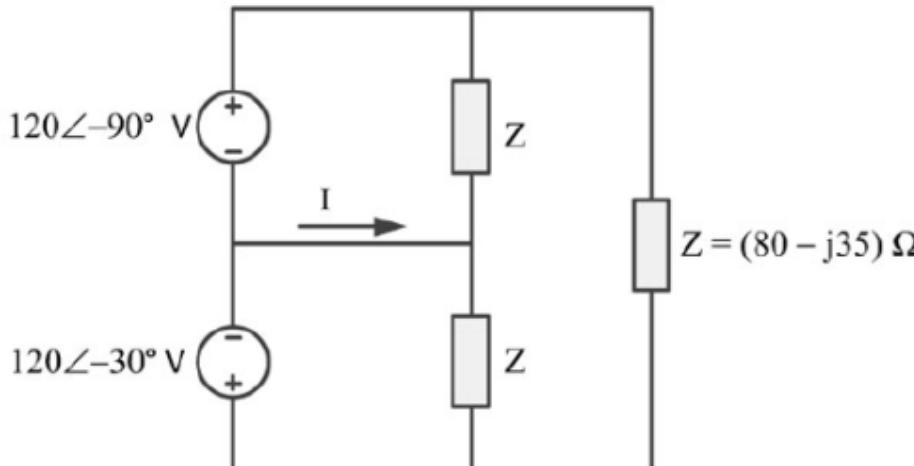
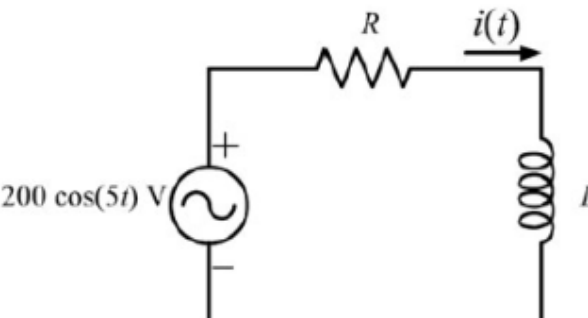
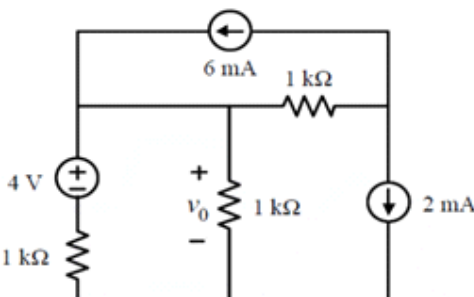
	the value of the load resistance $R_L$ is
(A) $R/4$	(B) $R/2$
(C) $R$	(D) $2R$

**2 mark sample questions**

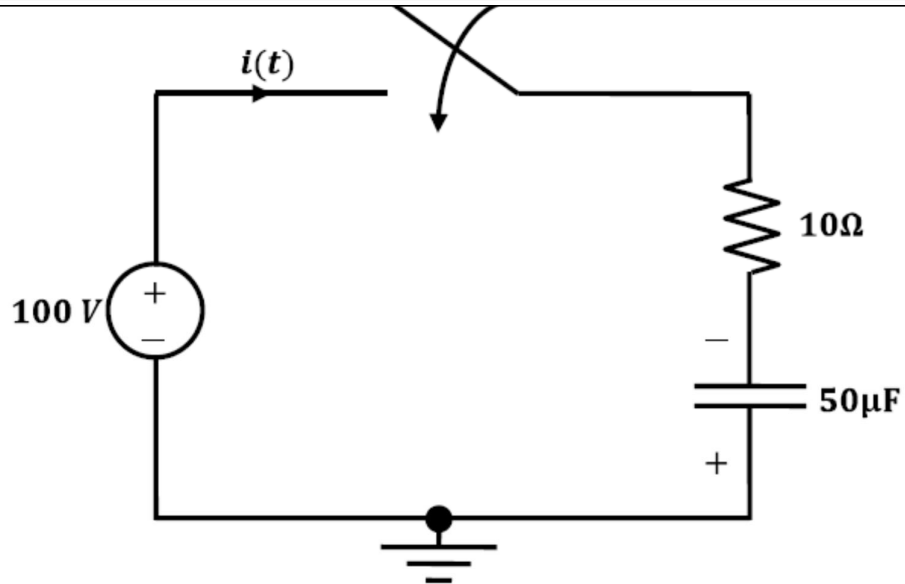
1)	<p>The switch p is in position P for a long time and then turn to position Q at <math>t=0</math>. Find the value of <math>dv(t)/dt</math> at <math>t=0+</math></p>
2)	<p>The switch is closed at <math>t=0</math> while the capacitor is charged to <math>-5V</math>. (i.e <math>V_c(0) = -5V</math>)</p>
3)	<p>The circuit in the figure contains a current source driving a load having an inductor and a resistor in series, with a shunt capacitor across the load. The ammeter is assumed to have zero resistance. The switch is closed at time <math>t = 0</math>.</p>

	<p>Initially, when the switch is open, the capacitor is discharged and the ammeter reads zero ampere. After the switch is closed, the ammeter reading keeps fluctuating for some time till it settles to a final steady value. The maximum ammeter reading that one will observe after the switch is closed (rounded off to two decimal places) is _____ A.</p>	
<p>4)</p>	<p>For the network shown, the equivalent Thevenin voltage and Thevenin impedance as seen across terminals 'ab' is</p>	
<p>5)</p>	<p>In the given circuit, for voltage <math>V_y</math> to be zero, the value of <math>\beta</math> should be _____. (Round off to 2 decimal places).</p>	
<p>6)</p>	<p>In the circuit, switch 'S' is in the closed position for a very long time. If the switch is opened at time <math>t = 0</math>, then <math>i_L(t)</math> in amperes, for <math>t \geq 0</math> is</p>	
<p>7)</p>	<p>In the circuit shown below, the Thevenin voltage <math>V_{TH}</math> is</p>	

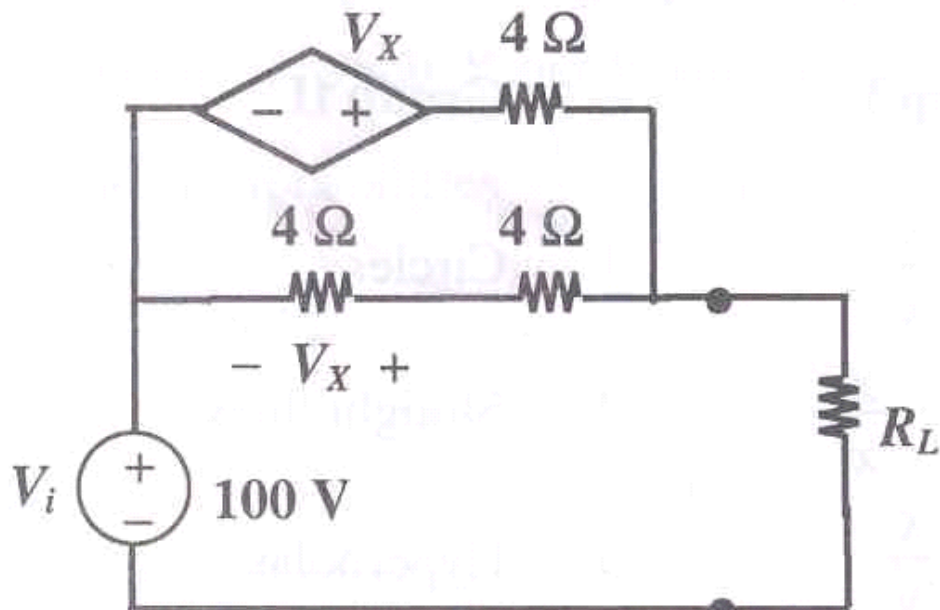


8)	<p>The current <math>I</math> in the given network is</p>  <p><math>Z = (80 - j35) \Omega</math></p>
9)	<p>The current in the RL-circuit shown below is <math>i(t) = 10 \cos(5t - \pi/4)</math> A. The value of the inductor (<b>rounded off to two decimal places</b>) is _____ H.</p> 
10)	<p>Consider the circuit shown in the figure.</p>  <p>The value of <math>v_0</math> (<b>rounded off to one decimal place</b>) is _____ V.</p>
11)	<p>In the circuit shown below, the initial charge on the capacitor is 2.5 mC, with the voltage polarity as indicated. The switch is closed at time <math>t = 0</math>. The current <math>i(t)</math> at a time <math>t</math> after the switch is closed is</p>



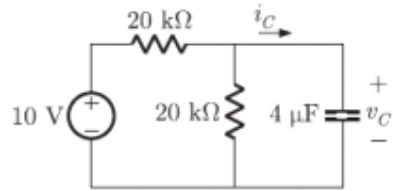
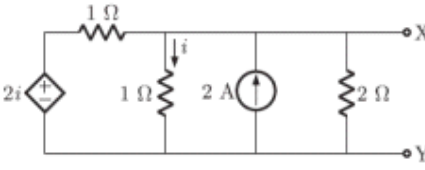
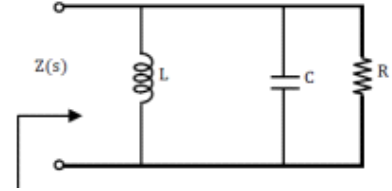


12) In the circuit shown, what value of  $R_L$  maximizes the power delivered to  $R_L$ ?



- (A)  $2.4 \Omega$       (B)  $\frac{8}{3} \Omega$       (C)  $4 \Omega$       (D)  $6 \Omega$

13) In the circuit shown,  $v_C$  is 0 volts at  $t = 0$  sec. For  $t > 0$ , the capacitor current  $i_C(t)$ , where  $t$  is in seconds is given by

	 <p>(A) <math>0.50 \exp(-25t)</math> mA                      (B) <math>0.25 \exp(-25t)</math> mA (C) <math>0.50 \exp(-12.5t)</math> mA                      (D) <math>0.25 \exp(-6.25t)</math> mA</p>
14)	<p>For the circuit shown in the figure, the Thevenin voltage and resistance looking into X – Y are</p>  <p>(A) <math>\frac{4}{3}</math> V, <math>2 \Omega</math>                      (B) 4 V, <math>\frac{2}{3} \Omega</math> (C) <math>\frac{4}{3}</math> V, <math>\frac{2}{3} \Omega</math>                      (D) 4 V, <math>2 \Omega</math></p>
15)	<p>The driving point impedance of the following network is given by <math>Z(s) = \frac{0.2s}{s^2 + 0.1s + 2}</math>. The component values are</p>  <p>(A) L = 5H, R = 0.5Ω, C = 0.1F                      (C) L = 0.1H, R = 2Ω, C = 0.1F (B) L = 0.1H, R = 0.5Ω, C = 5F                      (D) L = 0.1H, R = 2Ω, C = 5F</p>