

TCET

DEPARTMENT OF ELECTRONICS ENGINEERING (ETRX) Credit Based Grading Scheme(Revised - 2012) - University of Mumbai

CBGS-2012(R)

Semester Plan (Practical / Tutorials / Assignment)

Semester: III

Course: **B.E ETRX**

Batches: SE (E1/E2/E3/E4)

Subject: EDC-1

Class: T.E ETRX

Batch size: 20 Students

Laboratory faculty in charge: Ms. Poorva W. Lab. Assistant /Attendant: Ms.Sulbha Kashid (Lab Assistant 116)

Note: Experiment planned as per University Curriculum

Basic Experiments:

Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Planned Date	Completi on Date	Remarks			
1.	Prerequisite of EDC subject,	E1:18/7					
		E2:18/7					
2.	To implement PN junction diode,Zene Diode under FB and RB. Plot	er E1:25/7					
	characteristics and implement Zener regulator	E2:25/7					
3.	To implement the different types of clipp and Clamper circuits using discrete	er E1:1/8					
	components and using ORCAD	E2:1/8					
4	To implement CE amplifier as voltage	E1:8/8					
	parameters	E2:8/8					
5	To implement CS configuration of JFET and plot input -output characteristics	nd E1:5/9					
	plot input -output characteristics	E2:5/9					
Design/ Development Experiments:							
6.	To perform analysis and design of Fix	ed E1:12/9					
	amplifier	E2:12/9	_				
		E1:19/9					
7.	To design Full wave/Bridge rectifier with LC/pi filter	E2:19/9					
8.	Mini Project (groupwise)-Conceptualizat of idea	ion E1:26/9					
		E2:26/9					
9.	Mini Project (groupwise)-design and	E1:3/10					
Issuec	l By: MR	Approved By: Prin	roved By: Principal				





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	implementation E2:3/10											
10.	Mini Project (groupwise)- Testing a evaluation					E1: 17/10 &Extra hrs E2:17/10 & Extra hrs						
Group Learning Activity:												
11	Case Study:Use of Semiconductor D Electronics and comparative and				s in	E1						
12	Case Study: Applications of Optical and comparative study				ces	E2						
Mini /Minor Projects Objective: To get hands on experience to execute projects with respect to student choice in the following areas. (30 Hrs / Semester / Student). (Total 120 Hrs) The areas are : 1. Research 2. Core 3. Interdisciplinary 4. Application Mini /Minor Projects : 1.												
S.No	Project Title			Clas	S	Group Size/ Project Hours	Project Type		Reference			
1	To design CE amplifier			SE		3-4	Mini					
2	Applications based on Photodiode/Solar Cell			SE		3-4	Mini Technology		nology			
3	Voltage Regulator			SE		3-4	Mini	Based Learni		d Learning		
4	Applications of Semiconductor devices in commercial use			SE		3-4	Mini					
	Planned	Compl eted		Planned	ned	Completed		Plar	nned	Completed		
No. of Prac	Basic Exp: 04 Design Base Exp: 02 Bridge Course: 01 Mini Project: 4/1 per group Case study: 2 group wise		No. of Assignme nts	03			No. of Tut 01(l oria Prof I Stud		∟ow file dent)			
DOSLN	OSLNE: DOSL					(engaged in some other dates):						
 Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects etc. Note: The practical plan date and completion date shall be in compliance. For any non-compliance reason(s) required to be stated in remark column. Learning objective and outcome shall be clearly stated with each of experiments/ tutorials/ assignments and are required to be mapped at the end of the semester. Entry for DOSLE (engaged on some other date) shall be done with proper mapping to DOSLNE. 												
Name & Signature of Faculty Signature of HOD Signature of Principal / Dean Academic												
Issued By: MR Approved By: Principal												



Date: 11/07/2017